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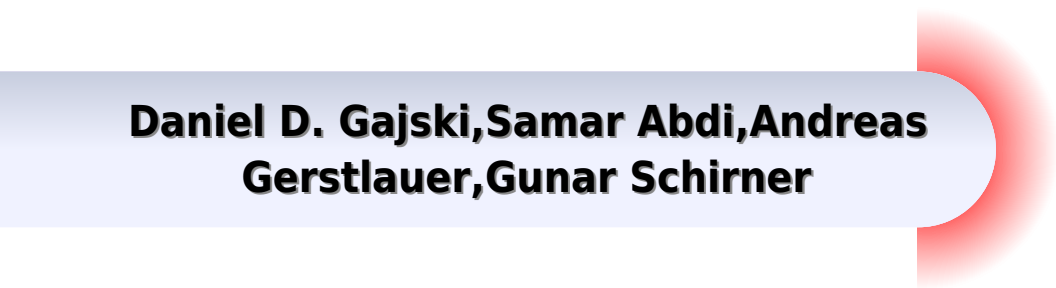
**Modeling,
Synthesis
and Verification**



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Embedded System Design Modeling Synthesis And Verification

**Daniel D. Gajski, Samar Abdi, Andreas
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