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Digital Systems and Applications Vojin G. Oklobdzija, 2017-12-19 New design architectures in computer systems have surpassed industry expectations Limits which were once thought of as fundamental have now been broken Digital Systems and Applications details these innovations in systems design as well as cutting edge applications that are emerging to take advantage of the field's increasingly sophisticated capabilities This book features new chapters on parallelizing iterative heuristics stream and wireless processors and lightweight embedded systems This fundamental text Provides a clear focus on computer systems architecture and applications Takes a top level view of system organization before moving on to architectural and organizational concepts such as superscalar and vector processor VLIW architecture as well as new trends in multithreading and multiprocessing includes an entire section dedicated to embedded systems and their applications Discusses topics such as digital signal processing applications circuit implementation aspects parallel I/O algorithms and operating systems Concludes with a look at new and future directions in computing Features articles that describe diverse aspects of computer usage and potentials for use Details implementation and performance enhancing techniques such as branch prediction register renaming and virtual memory Includes a section on new directions in computing and their penetration into many new fields and aspects of our daily lives

OS X Mountain Lion: The Missing Manual David Pogue, 2012-07-25 What do you get when you cross a Mac with an iPad OS X 10.8 Mountain Lion Its 200 new features include iPadish goodies like dictation Notification Center and Reminders but not a single page of instructions Fortunately David Pogue is back with the expertise and humor that have made this the 1 bestselling Mac book for over 10 years straight Big ticket changes Twitter and Facebook integration Air Play TV mirroring Power Nap Game Center Documents in the Cloud iMessages Gatekeeper If Apple wrote it this book covers it Mountain Lion Watch This book demystifies the hundreds of smaller enhancements too in all 50 programs that come with the Mac Safari Mail Messages Preview Shortcuts This must be the tippiest trickiest Mac book ever written Undocumented surprises await on every page Power users Security accounts networking build your own Services file sharing with Windows this one witty expert guide makes it all crystal clear There's something new on practically every page of this new edition and David Pogue brings his celebrated wit and expertise to every one of them Apple's brought a new cat to town and Mac OS X Mountain Lion The

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Signal And Image Processing Sourcebook Robert Libbey,1994-03-31 The technological background established in these early chapters especially in the production and processing of television images vividly illuminates the development of the sophisticated image processing employed in contemporary radar space exploration and medical radiological imaging Continuing this integrated approach the author links the fundamentals of analog telephony to the development of modern digital signal processing in telecommunications and networking A detailed account of microprocessor technology further integrates the overall picture of the field of contemporary signal and image processing Logically the discussion is extended to the aspects of signal processing involved in artificial intelligence and neural networks *Catalog of Copyright Entries. Third Series* Library of Congress. Copyright Office,1977 **Switching to the Mac: The Missing Manual, Mountain Lion**

Edition David Pogue,2012-09-14 Demonstrates how to become adjusted to the Macintosh operating system and how to transfer data from a Windows system to a Macintosh discussing topics such as moving files and Macintosh equivalents to Windows only programs **Memory Systems** Bruce Jacob,David Wang,Spencer Ng,2010-07-28 Is your memory hierarchy stopping your microprocessor from performing at the high level it should be Memory Systems Cache DRAM Disk shows you how to resolve this problem The book tells you everything you need to know about the logical design and operation physical design and operation performance characteristics and resulting design trade offs and the energy consumption of modern memory hierarchies You learn how to tackle the challenging optimization problems that result from the side effects that can appear at any point in the entire hierarchy As a result you will be able to design and emulate the entire memory hierarchy Understand all levels of the system hierarchy Xcache DRAM and disk Evaluate the system level effects of all design choices Model performance and energy consumption for each component in the memory hierarchy **Protocols for**

High-speed Networks, II Marjory J. Johnson, 1991 This book is the proceedings of a workshop which examined issues involved in the design and implementation of protocols for high speed networks The emphasis of the book is on protocol implementation with a large number of papers addressing this important topic Other topics addressed include evaluation of congestion flow control techniques that have been proposed for high speed networks new routing techniques and the investigation of protocols that are being designed to support high speed networking at the transport layer and at the media access control layer of the Open Systems Interconnection network model

The Microarchitecture of Pipelined and Superscalar Computers Amos R. Omondi, 2013-03-09 This book is intended to serve as a textbook for a second course in the implementation of microarchitectures of computer architectures The subject matter covered is the collection of techniques that are used to achieve the highest performance in single processor machines these techniques center the exploitation of low level parallelism temporal and spatial in the processing of machine instructions The target audience consists students in the final year of an undergraduate program or in the first year of a postgraduate program in computer science computer engineering or electrical engineering professional computer designers will also find the book useful as an introduction to the topics covered Typically the author has used the material presented here as the basis of a full semester undergraduate course or a half semester post graduate course with the other half of the latter devoted to multiple processor machines The background assumed of the reader is a good first course in computer architecture and implementation to the level in say Computer Organization and Design by D Patterson and H Hennessy and familiarity with digital logic design The book consists of eight chapters The first chapter is an introduction to all of the main ideas that the following chapters cover in detail the topics covered are the main forms of pipelining used in high performance uniprocessors a taxonomy of the space of pipelined processors and performance issues It is also intended that this chapter should be readable as a brief stand alone survey

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