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**Question Paper Code : 80122**

B.E./B.Tech. DEGREE EXAMINATIONS, APRIL/MAY 2019

Fourth Semester

Electronics and Communication Engineering

EC 8453 — LINEAR INTEGRATED CIRCUITS

(Common to B.E. Medical Electronics/B.E. Robotics and Automation Engineering/  
B.E. Biomedical Engineering)

(Regulation 2017)

Time : Three hours

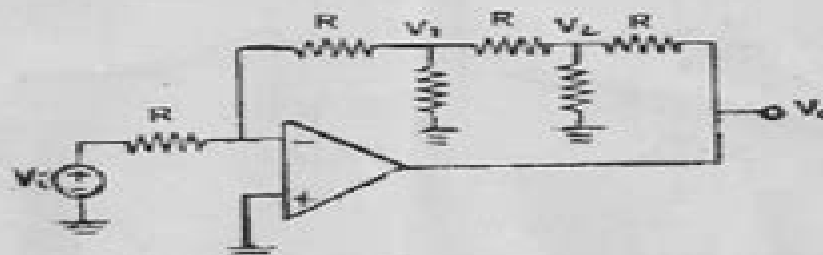
Maximum : 100 marks

(Codes / Tables / Charts to be permitted, if any may be indicated)

Answer ALL questions.

PART A — (10 × 2 = 20 marks)

1. State the significance of current mirror circuit.
2. Mention the application of LF155.
3. Find the gain of  $V_o/V_i$  of the circuit.



4. How does a zero crossing detector work?
5. What is Gilbert multiplier cell?
6. List the basic building blocks of PLL.

# Linear Integrated Circuit Sample Question Paper

**TD Snyder**



## **Linear Integrated Circuit Sample Question Paper:**

**2025-26 DFCCIL CBT-2 Executive Signal & Telecommunication Solved Papers & Practice Book** YCT Expert Team , 2025 26 DFCCIL CBT 2 Executive Signal Telecommunication Solved Papers Practice Book 240 495 E This book contains 4 sets of previous year solved papers and 10 sets of practice book      **Linear Integrated Circuits** Frederick Gans,1970      **Analog Circuit Design** Johan Huijsing,Rudy J. van de Plassche,Willy M.C. Sansen,2013-03-14 Johan H Huijsing This book contains 18 tutorial papers concentrated on 3 topics each topic being covered by 6 papers The topics are Low Noise Low Power Low Voltage Mixed Mode Design with CAD Tools Voltage Current and Time References The papers of this book were written by top experts in the field currently working at leading European and American universities and companies These papers are the reviewed versions of the papers presented at the Workshop on Advances in Analog Circuit Design which was held in Villach Austria 26 28 April 1995 The chairman of the Workshop was Dr Franz Dielacher from Siemens Austria The program committee existed of Johan H Huijsing from the Delft University of Technology Prof Willy Sansen from the Catholic University of Leuven and Dr Rudy 1 van der Plassche from Philips Eindhoven This book is the fourth of a series dedicated to the design of analog circuits The topics which were covered earlier were Operational Amplifiers Analog to Digital Converters Analog Computer Aided Design Mixed A/D Circuit Design Sensor Interface Circuits Communication Circuits Low Power Low Voltage Integrated Filters Smart Power As the Workshop will be continued year by year a valuable series of topics will be built up from all the important areas of analog circuit design I hope that this book will help designers of analog circuits to improve their work and to speed it up      **Electronic Design Automation for IC System Design, Verification, and Testing** Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-12-19 The first of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logic verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for integrated circuit IC designs design and verification languages digital simulation hardware acceleration and emulation and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on high level synthesis system on chip SoC block based design and back annotating system level models Offering improved depth and modernity Electronic Design Automation for IC System Design Verification and Testing provides a valuable state of the art reference for electronic design automation EDA students

researchers and professionals      **BSNL Jr. Engineer (TTA) Exam Guide + Practice Workbook (Concept Notes + 2 Solved + 10 Practice Sets) 2nd Edition** Disha Experts, 2017-09-01 The book BSNL TTA Exam Guide Practice Workbook Concept Notes 2 Solved 10 Practice Sets 2nd Edition has been specially designed to help students in the BSNL TTA exam Two fully solved past paper have been provided to guide you about the pattern and the level of questions asked The book covers theory material for Basic Engineering and Specilization Section to help in the preparation It also contains 2 past papers and 10 Practice Sets as per the pattern Each Practice Set is classified into 3 parts General Ability Test This part have 20 questions Basic Engineering This part have 90 questions and Specialization This part have 90 questions The questions in each practice set have been carefully selected so as to give you a real feel of the exam The book provides Response Sheet for each test Post each test you must do a Post Test Analysis with the help of the Test Analysis and Feedback Sheet which has been provided for each test      **IEEE VLSI Test Symposium** ,2005      **Reliability Abstracts and Technical Reviews**

,1970      **DCIS2002** Salvador Bracho del Pino, Mar Martínez, Teresa Riesgo, Miguel Ángel Allende Recio, 2002 Este libro contiene las presentaciones de la XVII Conferencia de Dise ño de Circuitos y Sistemas Integrados celebrado en el Palacio de la Magdalena Santander en noviembre de 2002 Esta Conferencia ha alcanzado un alto nivel de calidad como consecuencia de su tradici ñ y madurez que lo convierte en uno de los acontecimientos m s importantes para los circuitos de microelectr ñica y la comunidad de dise ño de sistemas en el sur de Europa Desde su origen tiene una gran contribuci ñ de Universidades espa ñolas aunque hoy los autores participan desde catorce pa ñes      **Scientific and Technical Aerospace Reports** ,1994

Design of Analog Integrated Circuits and Systems Kenneth R. Laker, Willy M. C. Sansen, 1994 It follows with a thorough treatment of design operational and operational transconductance amplifiers and concludes with a unified presentation of sample data and continuous time signal processing systems      Computer-Aided Design of Analog Integrated Circuits and Systems Rob A. Rutenbar, Georges G. E. Gielen, 2002-05-06 The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today s new analog design automation tools Areas covered are Analog synthesis Symbolic analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and yield optimization

Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck

**Development of CMOS-MEMS/NEMS Devices** Jaume Verd,Jaume Segura,2019-06-25 Micro and nano electro mechanical system M NEMS devices constitute key technological building blocks to enable increased additional functionalities within Integrated Circuits ICs in the More Than Moore era as described in the International Technology Roadmap for Semiconductors The CMOS ICs and M NEMS dies can be combined in the same package SiP or integrated within a single chip SoC In the SoC approach the M NEMS devices are monolithically integrated together with CMOS circuitry allowing the development of compact and low cost CMOS M NEMS devices for multiple applications physical sensors chemical sensors biosensors actuators energy actuators filters mechanical relays and others On chip CMOS electronics integration can overcome limitations related to the extremely low level signals in sub micrometer and nanometer scale electromechanical transducers enabling novel breakthrough applications This Special Issue aims to gather high quality research contributions dealing with MEMS and NEMS devices monolithically integrated with CMOS independently of the final application and fabrication approach adopted MEMS first interleaved MEMS MEMS last or others

**Publications** United States. National Bureau of Standards,1989

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