FLASHWAVE* 4500 Multiservice Provisioning Platform

Features and specifications

Synchronization

Architectures	Terminal Linear ADM (1-1) Unidirectional Path Switched Ring (UPSR) Two Fiber-Bidirectional Line Switched Ring (2F-BLSR) Four Fiber-Bidirectional Line Switched Ring (4F-BLSR) Dedicated Path Protected Mesh (DPPM) Resilient Packet Ring (RPR)		
Interfaces	051	64-pin AMP* CHAMP connectors	
	DS3/EC1	BNC connectors	
	OC-3/STM-1	SC for dual unit; LC for guad unit, MPO connector for 8-port unit 1310 nm wideband	
	OC-12/51M-4	LC, FC, SC or ST for single unit; LC for quad unit; MPO connector for 8-port unit 1310 nm wideband 1550 nm wideband (single unit only)	
	OC-48/\$TM-16	LC, FC, SC or ST connectors 1310 nm wideband 1550 nm tunable narrowband (8 λ @ 50 GHz)	
	OC-192/STM-64	LC, FC, SC or ST connectors 1310 nm wideband; 1550 nm wideband 1550 nm tunable narrowband (4 \(\lambda\) & 100 GHz) 1550 nm full band tunable to 88 \(\lambda\) (@ 50 GHz spacing) G.709 compliant	
	Ethernet (EoS)	RI-45 connectors for 10/100Base-T Ethernet; LC or SC connectors for Gigabit Ethernet	
	Ethernet Aggregation (trunking port, RPR)	LC connectors for dual port unit 850 nm wideband; 1310 nm wideband High/Low Order Aggregation	

Swelt chirry.

Maximum Switching Capacity				
Single	Main Shelf	Double Shell	Triple Shelf	
MSPP	MSSPIMSPP	MSSP	MSSP	
70 Gbps	140 Gbps	220 Gbps	300 Gbps	
1344 x 1344	2688 × 2688* 5760 × 5760**	5760 x 5760	5760 x 5760	
5376 x 5376	21,504 x 21,504	21,504 x 21,504	21,504 x 21,504	
(10 Gbps)	(40 Gbps)	(40 Gbps)	(40 Gbps)	
	Single	Single Main Shelf	Single	
	MSPP	MSPP MSSPIMSPP	MSPP Main Shelf	
	70 Gbps	70 Gbps 140 Gbps	MSSPMSPP Double Shelf	
	1344 x	1344 × 2688 × 2688*	MSSP 70 Gbps 140 Gbps 220 Gbps 1344 x	
	1344	1344 5760 × 5760**	1344 2688 x 2688*	
	5376 x 5376	5376 × 5376 21,504 × 21,504	5760 x 5760** 5760 x 5760 5376 x 5376 21,504 x 21,504 21,504 x 21,504	

	Requires.	140	Gbgrs swittch.	Earthree
men.	Branchises.	BOOK.	Charac quality by	Calburia.

Protection	DS1	1+1 or unprotected	
	DS3/EC1	1:n (n=1 to 6) or unprotected	
	DS3 Transmux	1+1 or 1:n (n-1 to 6)	
	OC-3/STM-1	1+1, UPSR, DPPM or unprotected	
	OC-12/58M-4	1+1, UPSR, DPPM or unprotected	
	OC-48/5TM-16	1+1, UPSR, 2F-BLSR, DPPM or unprotected	
	OC-192/STM-64	1+1, UPSR, 2F-BLSR, 4F-BLSR, DPPM or unprotected	
	Ethernet	RPR or unprotected Port Link Aggregation or Unit Link Aggregation	
	Switch matrices	1+1	
	Synchronization	1+1	

 DS1 Building Integrated Timing Supply (BITS) primary and secondary clock output/input
Line timing

Internal Stratum 3 timing source
 Synchronization Status Messaging (SSM)

Maximum Numbe	s of Unprote	cted (Protected):	Service Intest.	ne ers
Туре	Ports/ Card	Single Shelf MSPP/MSSP	Double Shelf MSSP	Triple Shelf MSSP
DS1 (Integrated)	28	84 (84)	168 (168)	252 (252)
DS1 (High-Density)	168	6720	13,440	20,160
DS3/EC1	8	96 (96)	192 (192)	288 (288)
DS3 Transmux	1.2	144	288	432
OC-3/3c/STM-1		160 (80)	320 (160)	480 (240)
OC-12/12o/STM-4	- 8	160 (80)	320 (160)	480 (240)
OC-48/48c/STM-16	2	40 (20)	80 (40)	120 (60)
OC-192/192c/STM-64	1	10 (5)	20 (10)	30 (15)
10r100Base-T Ethernet	4 (8)	80	160	240
Giosphit Ethermet		40	80	120

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