

DIGITAL INTEGRATED CIRCUITS

A DESIGN PERSPECTIVE
SECOND EDITION



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Power Aware Design Methodologies Massoud Pedram, Jan M. Rabaey, 2007-05-08 Power Aware Design Methodologies was conceived as an effort to bring all aspects of power aware design methodologies together in a single document. It covers several layers of the design hierarchy from technology circuit logic and architectural levels up to the system layer. It includes discussion of techniques and methodologies for improving the power efficiency of CMOS circuits digital and analog systems on chip microelectronic systems wirelessly networked systems of computational nodes and so on. In addition to providing an in depth analysis of the sources of power dissipation in VLSI circuits and systems and the technology and design trends this book provides a myriad of state of the art approaches to power optimization and control. The different chapters of Power Aware Design Methodologies have been written by leading researchers and experts in their respective areas. Contributions are from both academia and industry. The contributors have reported the various technologies methodologies and techniques in such a way that they are understandable and useful.

Solutions Manual Jan Rabaey Staff, **Building Embedded Systems** Changyi Gu, 2016-05-26 Develop the software and hardware you never think about. We're talking about the nitty gritty behind the buttons on your microwave inside your thermostat inside the keyboard used to type this description and even running the monitor on which you are reading it now. Such stuff is termed embedded systems and this book shows how to design and develop embedded systems at a professional level. Because yes many people quietly make a successful career doing just that. Building embedded systems can be both fun and intimidating. Putting together an embedded system requires skill sets from multiple engineering disciplines from software and hardware in particular. Building Embedded Systems is a book about helping you do things in the right way from the beginning of your first project. Programmers who know software will learn what they need to know about hardware. Engineers with hardware knowledge likewise will learn about the software side. Whatever your background is Building Embedded Systems is the perfect book to fill in any knowledge gaps and get you started in a career programming for everyday devices. Author Changyi Gu brings more than fifteen years of experience in working his way up the ladder in the field of embedded systems. He brings knowledge of numerous approaches to embedded systems design including the System on Programmable Chips (SOPC) approach that is currently growing to dominate the field. His knowledge and experience make Building Embedded Systems an excellent book for anyone wanting to enter the field or even just to do some embedded programming as a side project.

What You Will Learn Program embedded systems at the hardware level. Learn current industry practices in firmware development. Develop practical knowledge of embedded hardware options. Create tight integration between software and hardware. Practice a work flow leading to successful outcomes. Build from transistor level to the system level. Make sound choices between performance and cost. Who This Book Is For Embedded system engineers and intermediate electronics enthusiasts who are seeking tighter integration between software and hardware. Those who favor the System on a Programmable Chip (SOPC) approach will in particular benefit from

this book Students in both Electrical Engineering and Computer Science can also benefit from this book and the real life industry practice it provides

VLSI Signal Processing, VII Jan Rabaey, Paul M. Chau, John Eldon, 1994

Low Power Design Essentials Jan Rabaey, 2009-04-21 Low Power Design Essentials contains all the topics of importance to the low power designer The book lays the foundation with background chapters entitled Advanced MOS Transistors and Their Models and Power Basics These chapters are followed by chapters on the design process including optimization architecture and algorithm level memory run time standby logic and standby memory Chapters on special topics are also included power management and modal design ultra low power and low power design methodology and flows The book concludes with a chapter on case studies as well as a chapter on Projection into the Future These chapters are all based on the extensive amount of teaching that the author has carried out both at universities and companies worldwide All chapters have been drawn up specifically for self study They aim however at different levels of understanding All the chapters start with elementary material but most also contain advanced material

FinFETs and Other Multi-Gate Transistors J.-P. Colinge, 2008 This book explains the physics and properties of multi gate field effect transistors MuGFETs how they are made and how circuit designers can use them to improve the performances of integrated circuits It covers the emergence of quantum effects due to the reduced size of the devices and describes the evolution of the MOS transistor from classical structures to SOI silicon on insulator and then to MuGFETs

Computers as Components Marilyn Wolf, 2012-06-12

Computers as Components Principles of Embedded Computing System Design Third Edition presents essential knowledge on embedded systems technology and techniques Updated for today s embedded systems design methods this volume features new examples including digital signal processing multimedia and cyber physical systems It also covers the latest processors from Texas Instruments ARM and Microchip Technology plus software operating systems networks consumer devices and more Like the previous editions this textbook uses real processors to demonstrate both technology and techniques shows readers how to apply principles to actual design practice stresses necessary fundamentals that can be applied to evolving technologies and helps readers gain facility to design large complex embedded systems Updates in this edition include description of cyber physical systems exploration of the PIC and TI OMAP processors high level representations of systems using signal flow graphs enhanced material on interprocess communication and buffering in operating systems and design examples that include an audio player digital camera and cell phone The author maintains a robust ancillary site at <http://www.marilynwolf.us/CaC3e/index.html> which includes a variety of support materials for instructors and students including PowerPoint slides for each chapter lab assignments developed for multiple systems including the ARM based BeagleBoard computer downloadable exercises solutions and source code and links to resources and additional information on hardware software systems and more This book will appeal to students in an embedded systems design course as well as to researchers and savvy professionals schooled in hardware or software design Description of cyber physical systems physical systems with

integrated computation to give new capabilities Exploration of the PIC and TI OMAP multiprocessors High level representations of systems using signal flow graphs Enhanced material on interprocess communication and buffering in operating systems Design examples include an audio player digital camera cell phone and more

Computers as Components Wayne Hendrix Wolf, 2005 This work unravels the complexity of embedded systems e g cell phones microwaves and information appliances and of the process tools and techniques necessary for designing them

Adaptive Techniques for Dynamic Processor Optimization Alice Wang, Samuel Naffziger, 2008-07-23 The integrated circuit has evolved tremendously in recent years as Moore's Law has enabled exponentially more devices and functionality to be packed onto a single piece of silicon In some ways however these highly integrated circuits of which microprocessors are the flagship example have become victims of their own success Despite dramatic reductions in the switching energy of the transistors these reductions have kept pace neither with the increased integration levels nor with the higher switching frequencies In addition the atomic dimensions being utilized by these highly integrated processors have given rise to much higher levels of random and systematic variation which undercut the gains from process scaling that would otherwise be realized So these factors the increasing impact of variation and the struggle to control power consumption have given rise to a tremendous amount of innovation in the area of adaptive techniques for dynamic processor optimization The fundamental premise behind adaptive processor design is the recognition that variations in manufacturing and environment cause a statically configured operating point to be far too inefficient Inefficient designs waste power and performance and will quickly be surpassed by more adaptive designs just as it happens in the biological realm Organisms must adapt to survive and a similar trend is seen with processors those that are enabled to adapt to their environment will be far more competitive

Application Specific Processors for Numerical Algorithms Lars Erik Thon, 1992

mm-Wave Silicon Technology Ali M. Niknejad, Hossein Hashemi, 2008-01-03 This book compiles and presents the research results from the past five years in mm wave Silicon circuits This area has received a great deal of interest from the research community including several university and research groups The book covers device modeling circuit building blocks phased array systems and antennas and packaging It focuses on the techniques that uniquely take advantage of the scale and integration offered by silicon based technologies

Ultra Wideband Ranjit Gharpurey, Peter Kinget, 2008-03-06 Recent advances in wireless communication technologies have had a transformative impact on society and have directly contributed to several economic and social aspects of daily life Increasingly the untethered exchange of information between devices is becoming a prime requirement for further progress which is placing an ever greater demand on wireless bandwidth The ultra wideband UWB system marks a major milestone in this progress Since 2002 when the FCC allowed the unlicensed use of low power UWB radio signals in the 3.1-10.6 GHz frequency band there has been significant synergistic advance in this technology at the circuits architectural and communication systems levels This technology allows for devices to communicate wirelessly while coexisting with other users

by ensuring that its power density is sufficiently low so that it is perceived as noise to other users UWB is expected to address existing needs for high data rate short range communication applications between devices such as computers and peripherals or consumer electronic devices In the long term it makes available spectrum to experiment with new signaling formats such as those based on very short pulses of radio frequency RF energy As such it represents an opportunity to design fundamentally different wireless systems which rely on the bandwidth of the signals to enhance the data rate or which use the available bandwidth for diverse applications such as ranging and biomedical instrumentation

Creating Assertion-Based IP Harry D. Foster, Adam C. Krolnik, 2007-11-26 This book presents formal testplanning guidelines with examples focused on creating assertion based verification IP It demonstrates a systematic process for formal specification and formal testplanning and also demonstrates effective use of assertions languages beyond the traditional language construct discussions Note that there are many books published on assertion languages such as SystemVerilog assertions and PSL Yet none of them discuss the important process of testplanning and using these languages to create verification IP This is the first book published on this subject

Advances in Design Hoda A. ElMaraghy, Waguih H. ElMaraghy, 2006-07-02 Advances in Design examines recent advances and innovations in product design paradigms methods tools and applications It presents fifty two selected papers which were presented at the 14th CIRP International Design Seminar held in May 2004 as well as the invited keynote papers Dr Waguih ElMaraghy was the conference Chair and Dr Hoda ElMaraghy was on the program committee The International Institution for Production Research CIRP founded in 1951 is the top production engineering research college worldwide The CIRP is subdivided into Scientific and Technical Committees STCs which are responsible for coordinating cutting edge research as well as holding highly regarded annual international seminars to disseminate the results The CIRP Design STC meeting is the forum in which the latest developments in the design field are presented and discussed

Design for Manufacturability and Statistical Design Michael Orshansky, Sani Nassif, Duane Boning, 2007-10-28 Design for Manufacturability and Statistical Design A Constructive Approach provides a thorough treatment of the causes of variability methods for statistical data characterization and techniques for modeling analysis and optimization of integrated circuits to improve yield The objective of the constructive approach developed in this book is to formulate a consistent set of methods and principles necessary for rigorous statistical design and design for manufacturability from device physics to large scale circuit optimization The segments of the book are devoted respectively to understanding the causes of variability design of test structures for variability characterization statistically rigorous data analysis techniques of design for manufacturability in lithography and in chemical mechanical polishing statistical simulation analysis and optimization techniques for improving parametric yield Design for Manufacturability and Statistical Design A Constructive Approach presents an overview of the methods that need to be mastered for state of the art design for manufacturability and statistical design methodologies It is an important reference for practitioners and students in the field of computer aided design of integrated circuits

The

British National Bibliography Arthur James Wells,1996 *Design for Manufacturability and Yield for Nano-Scale CMOS*
Charles Chiang,Jamil Kawa,2007-06-15 This book walks the reader through all the aspects of manufacturability and yield in a nano CMOS process It covers all CAD CAE aspects of a SOC design flow and addresses a new topic DFM DFY critical at 90 nm and beyond This book is a must read book the serious practicing IC designer and an excellent primer for any graduate student intent on having a career in IC design or in EDA tool development **14th Symposium on Integrated Circuits and Systems Design** Sociedade Brasileira de Computação,2001 Annotation Papers from a September 2001 symposium report on recent advances in areas of integrated circuits and systems design including embedded systems rapid prototyping formal methods codesign CAD and test analog digital and physical design and low power and low voltage Specific topics include communication architectures for system on chip using the CAN protocol and reconfigurable computing technology for Web based smart house automation and optimizing BBD based verification analyzing variable dependencies Other subjects include interconnection length estimation at logic level an environment to aid the synthesis of threephase analogue waveform using AHDL and extending sequencing graphs for reconfigurable applications modeling This work lacks a subject index c Book News Inc **ACM SIGPLAN Notices** ,2003-07 **Forthcoming Books** Rose Arny,2002-02

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