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Name :

Third Semester B.Tech. Engineering Degree (Regular/Supplementary) Examination, November 2010 2K6 CS/IT 305 : SWITCHING THEORY AND LOGIC DESIGN

Time: 3 Hours

Max. Marks: 100

Instruction: Answer all questions.

- a) Give symbols and truth tables of electronic gates and contact networks.
 - b) Define sum of products and product of sum with examples.
- c) What are universal gates and its properties? Give suitable example.
 - d) Write a note on look ahead adders.
 - e) What do you mean by path sensitisation?
 - f) What do you mean by programmable logic arrays? Give example.
 - g) Write a note on Flip-flops. Give truth table for SR and JK Flip-flops.
 - h) What do you mean by synchronous counter? How it differs from asynchronous counter? (5×8=40)
- 2. A) i) Simplify following K-map using sum of products.

10

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10	1	1	1	1

verify using truth table.

What do you mean by normal and canonical forms? Give suitable example.

5

High Speed Switching Architecture University Question Paper

Allan Gottlieb, Yao Li, Eugen Schenfeld

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Japan Telecommunications ,1988 **Technical Program, Conference Record**, 1999 Conference Record ,2001 Proceedings of the ... Conference on Information Sciences and Systems ,1994 IEEE International Symposium on **Conference Proceedings** ,1989 The Railway and Engineering Review Walter Mason 1994 IEEE 13th Annual International Phoenix Conference on Computers and Communications Camp.1910 Networks 2004 Hermann Kaindl, 2004 **Providing Quality of Service in Heterogeneous Environments Ralf** ,1994 Lehnert, Phuoc Tran-Gia, Joachim Charzinsky, 2003 Fourth IEE Conference on Telecommunications, 18-21 April 1993 Institution of Electrical Engineers. Electronics Division, 1993 Optical Network Design and Modeling Ioannis Tomkos, Fabio Neri, Josep Solé-Pareta, Xavier Masip-Bruin, Sergi Sánchez-López, 2007-07-21 This book constitutes the refereed proceedings of the 11th International IFIP TC6 Conference on Optical Network Design and Modeling ONDM 2007 held in Athens Greece in May 2007 The 41 revised full papers presented together with 14 invited papers address all recent advances in the design modeling and implementation of optical networks Scientific Information Bulletin ,1992 **Communication and** Architectural Support for Network-Based Parallel Computing Dhabaleswar K. Panda, Craig B. Stunkel, 1997-01-24 This book constitutes the refereed proceedings of the First International Workshop on Communication and Architectural Support for Network Based Parallel Computing CANPC 97 held in San Antonio Texas USA in February 1997 The 19 revised full papers presented were carefully selected from a total of 36 submissions Among the topics addressed are processor network interfaces communication protocols high performance network technology operating systems and architectural issues and load balancing techniques All in all the papers competently describe the state of the art for network based computing systems **Proceedings of the Third International Conference on Massively Parallel Processing Using Optical Interconnections** Allan Gottlieb, Yao Li, Eugen Schenfeld, 1996 Contains papers from the October 1996 conference focusing on the potentials and impacts that optical interconnections can have on massively parallel processing Offers sections on comparative studies for optical interconnects interconnection networks and system architectures WDM in MPP systems tradeoffs in intra system optical interconnects scalable interconnection networks architecture issues guided wave components for optical interconnects and multiprocessor networks and systems Includes discussion of the roles of university and industry in developing systems For researchers in computer science engineering and optics No index Annotation **Applications of Photonic Technology** J. Chrostowski, G.A. copyrighted by Book News Inc Portland OR Lampropoulos, R.M. Measures, 2013-06-29 In this book we present a snapshot of the state of the art in photonics in 1994 showing typical applications and emerging new ones discussing the key technologies behind these applications their limitations and prospects The articles in this book are extended versions of the papers presented at the first International Conference on Applications of Photonic Technology ICAPT 94 held in Toronto Canada on June 21 23 1994 Photonics has been

recognized as one of the key technologies for the 21 st century as electronics was the technology of the 20th centrury and electrical engineering changed the life of people in the 19th century According to the recent report of the Organization for Economic Cooperation and Development in Paris OECD the market for photonics will grow dramatically in the next 10 years with an expected world wide expenditure of US 230 billion from some US 30 billion in 1992 The explosion of information technology was the largest driving force for the deployment of photonic technology It created insatiable demand for ever higher data transmission and processing rates which cannot be sustained by electronics alone Boosted by the enonnous investment of the telecommunications and defense industries the demand for photonics or optoelectronics is steadily increasing It is solidly established in the long haul communications laser printers and CD ROMs *Physics Briefs*, 1993

High-Performance Backbone Network Technology Naoaki Yamanaka,2020-04-01 Compiling the most influential papers from the IEICE Transactions in Communications High Performance Backbone Network Technology examines critical breakthroughs in the design and provision of effective public service networks in areas including traffic control telephone service real time video transfer voice and image transmission for a content delivery network CDN and Internet access The contributors explore system structures experimental prototypes and field trials that herald the development of new IP networks that offer quality of service QoS as well as enhanced security reliability and function Offers many hints and guidelines for future research in IP and photonic backbone network technologies The Second RIKEN International Symposium on Symbolic and Algebraic Computation by Computers Nobuyuki Inada, Takashi S?ma,1985 This proceedings is based on research work on formula manipulation and computer algebra culminating in the design and construction of a formula manipulation machine at RIKEN known as the FLATS project Masters Abstracts International ,1991

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