

# CHAPTER 2

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2-1.\*

a)  $\overline{XYZ} = \overline{X} + \overline{Y} + \overline{Z}$

Verification of DeMorgan's Theorem

X	Y	Z	XYZ	$\overline{XYZ}$	$\overline{X} + \overline{Y} + \overline{Z}$
0	0	0	0	1	1
0	0	1	0	1	1
0	1	0	0	1	1
0	1	1	0	1	1
1	0	0	0	1	1
1	0	1	0	1	1
1	1	0	0	1	1
1	1	1	1	0	0

b)  $\overline{X + YZ} = (\overline{X + Y})(\overline{X + Z})$

The Second Distributive Law

X	Y	Z	YZ	$\overline{X + YZ}$	$\overline{X + Y}$	$\overline{X + Z}$	$(\overline{X + Y})(\overline{X + Z})$
0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0
0	1	0	0	0	1	0	0
0	1	1	1	1	1	1	1
1	0	0	0	1	1	1	1
1	0	1	0	1	1	1	1
1	1	0	0	1	1	1	1
1	1	1	1	1	1	1	1

c)  $\overline{XY} + \overline{YZ} + \overline{XZ} = \overline{XY + YZ + XZ}$

X	Y	Z	$\overline{XY}$	$\overline{YZ}$	$\overline{XZ}$	$\overline{XY + YZ + XZ}$	$\overline{XY}$	$\overline{YZ}$	$\overline{XZ}$	$\overline{XY + YZ + XZ}$
0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	1	0	1	0	0	1	1
0	1	0	1	0	0	1	0	1	0	1
0	1	1	1	0	0	1	0	0	1	1
1	0	0	0	0	1	1	1	0	0	1
1	0	1	0	1	0	1	1	0	0	1
1	1	0	0	0	1	1	0	1	0	1
1	1	1	0	0	0	0	0	0	0	0

2-2.\*

a)  $\overline{X}\overline{Y} + \overline{X}Y + X\overline{Y} = \overline{X} + \overline{Y}$   
 $= (\overline{X}\overline{Y} + \overline{X}Y) + (\overline{X}\overline{Y} + X\overline{Y})$   
 $= \overline{X}(\overline{Y} + Y) + \overline{Y}(\overline{X} + X)$   
 $= \overline{X} + \overline{Y}$

b)  $\overline{A}B + \overline{B}\overline{C} + AB + \overline{B}C = 1$   
 $= (\overline{A}B + AB) + (\overline{B}\overline{C} + \overline{B}C)$   
 $= B(A + \overline{A}) + \overline{B}(C + \overline{C})$

# Logic Computer Design Fundamentals 4th Solution Manual

**Y Pai**



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Fundamentals of Digital Logic and Microcomputer Design M. Rafiquzzaman, 2005-07-08 Fundamentals of Digital Logic and Microcomputer Design has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers In this Fifth Edition the author focuses on computer design at three levels the device level the logic level and the system level Basic topics are covered such as number systems and Boolean algebra combinational and sequential logic design as well as more advanced subjects such as assembly language programming and microprocessor based system design Numerous examples are provided throughout the text Coverage includes Digital circuits at the gate and flip flop levels Analysis and design of combinational and sequential circuits Microcomputer organization architecture and programming concepts Design of computer instruction sets CPU memory and I/O System design features associated with popular microprocessors from Intel and Motorola Future plans in microprocessor development An instructor's manual available upon request Additionally the accompanying CD ROM contains step by step procedures for installing and using Altera Quartus II software MASM 6.11 8086 and 68asm sim 68000 provides valuable simulation results via screen shots Fundamentals of Digital Logic and Microcomputer Design is an essential reference that will provide you with the fundamental tools you need to design typical digital systems **Subject Guide to Books in**

**Print** ,1996      **Scientific and Technical Books in Print** ,1972      *Computer Books and Serials in Print* ,1985      *Books in Print* ,1994      Computer Organization, Design, and Architecture Sajjan G. Shiva,2007-11-30 Suitable for a one or two semester undergraduate or beginning graduate course in computer science and computer engineering Computer Organization Design and Architecture Fourth Edition presents the operating principles capabilities and limitations of digital computers to enable development of complex yet efficient systems With 40% upd      **The Publishers' Trade List Annual** ,1985      *Scientific and Technical Aerospace Reports* ,1995      **Embedded SoPC Design with Nios II Processor and Verilog Examples** Pong P. Chu,2012-05-14 Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog An SoPC system on a programmable chip integrates a processor memory modules I O peripherals and custom hardware accelerators into a single FPGA field programmable gate array device In addition to the customized software customized hardware can be developed and incorporated into the embedded system as well allowing us to configure the soft core processor create tailored I O interfaces and develop specialized hardware accelerators for computation intensive tasks Utilizing an Altera FPGA prototyping board and its Nios II soft core processor Embedded SoPC Design with Nios II Processor and Verilog Examples takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board Emphasizing hardware design and integration throughout the book is divided into four major parts Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I O peripherals including a PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides several case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology      **Microelectronic Design of Fuzzy Logic-Based Systems** Iluminada Baturone,Angel Barriga,Carlos Jimenez-Fernandez,Diego R. Lopez,Santiago Sanchez-Solano,2000-03-30 Fuzzy logic has virtually exploded over the landscape of emerging technologies becoming an integral part of myriad applications and a standard tool for engineers Until recently most of the attention and applications have centered on fuzzy systems implemented in software But these systems are limited Problems that require real time operation low area or low power consumption demand hardware designed to the fuzzy paradigm and engineers with the background and skills to design it Microelectronic Design of Fuzzy Logic Based Systems offers low cost answers to issues that software cannot resolve From the theoretical architectural and technological foundation to design tools and

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Copyright Office,1968     **Ten Days with 8085 MICROPROCESSOR** K. A. KRISHNAMURTHY,Krishnamurthy K. A.,2010-01-01 This text is designed to provide hands on experience to students to help them develop an understanding of the hardware components of a microprocessor and the role of software in programming and interfacing aspects of the microprocessor An 8 bit microprocessor due to its simpler instruction set and architecture is an ideal IC chip for providing the students with a solid foundation for micro processors their principles and applications The concepts of all state of the art processors can be understood easily once the basics of the 8085 are understood Today s sophisticated microprocessors have a semblance of 8085 The presentation style adopted in this book in a way is unique It is a student friendly text written as conversation between the teacher and the students The book lucidly explains the various programming examples in assembly language with a view to enabling students to develop microprocessor based industrial application projects Application programs developed in the book are based on the popular microprocessor kit namely SDA 85 The book is suitable for both diploma and degree level students pursuing courses in Electronics and Electrical Engineering Electronics and Communication Engineering and Information and Communication Technology     Publishers' Trade List Annual, 1980 Bobbs-Merrill Educational Co,1980     **British Books in Print** ,1986     Whitaker's Cumulative Book List ,1984

*Forthcoming Books* Rose Army,2003     **Ref Man Logicaid Cad Users Win** Roth, J.D.,Charles H. Roth,1992-06 This is a computer aided logic design program that enables readers to easily derive logic equations from interns truth tables and state tables and provides tutorial help for Karnaugh maps and derivation of state graphs

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