

IBIS MODELING COOKBOOK

For IBIS Version 4.0

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The IBIS Open Forum

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Ibis Modeling Cookbook For Ibis Version 4

**Arthur H.M. van Roermund, Herman
Casier, Michiel Steyaert**



Ibis Modeling Cookbook For Ibis Version 4:

Timing Analysis and Simulation for Signal Integrity Engineers Greg Edlund, 2007-10-22 Every day companies call upon their signal integrity engineers to make difficult decisions about design constraints and timing margins Can I move these wires closer together How many holes can I drill in this net How far apart can I place these chips Each design is unique there s no single recipe that answers all the questions Today s designs require ever greater precision but design guides for specific digital interfaces are by nature conservative Now for the first time there s a complete guide to timing analysis and simulation that will help you manage the tradeoffs between signal integrity performance and cost Writing from the perspective of a practicing SI engineer and team lead Greg Edlund of IBM presents deep knowledge and quantitative techniques for making better decisions about digital interface design Edlund shares his insights into how and why digital interfaces fail revealing how fundamental sources of pathological effects can combine to create fault conditions You won t just learn Edlund s expert techniques for avoiding failures you ll learn how to develop the right approach for your own projects and environment Coverage includes Systematically ensure that interfaces will operate with positive timing margin over the product s lifetime without incurring excess cost Understand essential chip to chip timing concepts in the context of signal integrity Collect the right information upfront so you can analyze new designs more effectively Review the circuits that store information in CMOS state machines and how they fail Learn how to time common clock source synchronous and high speed serial transfers Thoroughly understand how interconnect electrical characteristics affect timing propagation delay impedance profile crosstalk resonances and frequency dependent loss Model 3D discontinuities using electromagnetic field solvers Walk through four case studies coupled differential vias land grid array connector DDR2 memory data transfer and PCI Express channel Appendices present a refresher on SPICE modeling and a high level conceptual framework for electromagnetic field behavior Objective realistic and practical this is the signal integrity resource engineers have been searching for Preface xiii Acknowledgments xvi About the Author xix About the Cover xx Chapter 1 Engineering Reliable Digital Interfaces 1 Chapter 2 Chip to Chip Timing 13 Chapter 3 Inside IO Circuits 39 Chapter 4 Modeling 3D Discontinuities 73 Chapter 5 Practical 3D Examples 101 Chapter 6 DDR2 Case Study 133 Chapter 7 PCI Express Case Study 175 Appendix A A Short CMOS and SPICE Primer 209 Appendix B A Stroll Through 3D Fields 219 Endnotes 233 Index 235 **Power Integrity for I/O Interfaces**

Vishram S. Pandit, Woong Hwan Ryu, Myoung Joon Choi, 2010-10-13 Foreword by Joungho Kim The Hands On Guide to Power Integrity in Advanced Applications from Three Industry Experts In this book three industry experts introduce state of the art power integrity design techniques for today s most advanced digital systems with real life system level examples They introduce a powerful approach to unifying power and signal integrity design that can identify signal impediments earlier reducing cost and improving reliability After introducing high speed single ended and differential I O interfaces the authors describe on chip package and PCB power distribution networks PDNs and signal networks carefully reviewing their

interactions Next they walk through end to end PDN and signal network design in frequency domain addressing crucial parameters such as self and transfer impedance They thoroughly address modeling and characterization of on chip components of PDNs and signal networks evaluation of power to signal coupling coefficients analysis of Simultaneous Switching Output SSO noise and many other topics Coverage includes The exponentially growing challenge of I O power integrity in high speed digital systems PDN noise analysis and its timing impact for single ended and differential interfaces Concurrent design and co simulation techniques for evaluating all power integrity effects on signal integrity Time domain gauges for designing and optimizing components and systems Power signal integrity interaction mechanisms including power noise coupling onto signal trace and noise amplification through signal resonance Performance impact due to Inter Symbol Interference ISI crosstalk and SSO noise as well as their interactions Validation techniques including low impedance VNA measurements power noise measurements and characterization of power to signal coupling effects Power Integrity for I O Interfaces will be an indispensable resource for everyone concerned with power integrity in cutting edge digital designs including system design and hardware engineers signal and power integrity engineers graduate students and researchers

Silicon Photonics III Lorenzo Pavesi, David J. Lockwood, 2016-01-08 This book is volume III of a series of books on silicon photonics It reports on the development of fully integrated systems where many different photonics component are integrated together to build complex circuits This is the demonstration of the fully potentiality of silicon photonics It contains a number of chapters written by engineers and scientists of the main companies research centers and universities active in the field It can be of use for all those persons interested to know the potentialities and the recent applications of silicon photonics both in microelectronics telecommunication and consumer electronics market Advanced Signal Integrity for High-Speed Digital Designs Stephen H. Hall, Howard L. Heck, 2011-09-20 A synergistic approach to signal integrity for high speed digital design This book is designed to provide contemporary readers with an understanding of the emerging high speed signal integrity issues that are creating roadblocks in digital design Written by the foremost experts on the subject it leverages concepts and techniques from non related fields such as applied physics and microwave engineering and applies them to high speed digital design creating the optimal combination between theory and practical applications Following an introduction to the importance of signal integrity chapter coverage includes Electromagnetic fundamentals for signal integrity Transmission line fundamentals Crosstalk Non ideal conductor models including surface roughness and frequency dependent inductance Frequency dependent properties of dielectrics Differential signaling Mathematical requirements of physical channels S parameters for digital engineers Non ideal return paths and via resonance I O circuits and models Equalization Modeling and budgeting of timing jitter and noise System analysis using response surface modeling Each chapter includes many figures and numerous examples to help readers relate the concepts to everyday design and concludes with problems for readers to test their understanding of the material Advanced Signal Integrity for High Speed Digital

Designs is suitable as a textbook for graduate level courses on signal integrity for programs taught in industry for professional engineers and as a reference for the high speed digital designer

Surrogate-Based Modeling and Optimization Slawomir Koziel, Leifur Leifsson, 2013-06-06 Contemporary engineering design is heavily based on computer simulations. Accurate high fidelity simulations are used not only for design verification but even more importantly to adjust parameters of the system to have it meet given performance requirements. Unfortunately accurate simulations are often computationally very expensive with evaluation times as long as hours or even days per design making design automation using conventional methods impractical. These and other problems can be alleviated by the development and employment of so called surrogates that reliably represent the expensive simulation based model of the system or device of interest but they are much more reasonable and analytically tractable. This volume features surrogate based modeling and optimization techniques and their applications for solving difficult and computationally expensive engineering design problems. It begins by presenting the basic concepts and formulations of the surrogate based modeling and optimization paradigm and then discusses relevant modeling techniques, optimization algorithms and design procedures as well as state of the art developments. The chapters are self contained with basic concepts and formulations along with applications and examples. The book will be useful to researchers in engineering and mathematics in particular those who employ computationally heavy simulations in their design work.

Real-Time Modelling and Processing for Communication Systems Muhammad Alam, Wael Dghais, Yuanfang Chen, 2017-12-27 This book presents cutting edge work on real time modelling and processing a highly active research field in both the research and industrial domains. Going beyond conventional real time systems major efforts are required to develop accurate and computationally efficient real time modelling algorithms and design automation tools that reflect the technological advances in high speed and ultra low power transceiver communication architectures based on nanoscale devices. The book addresses basic and more advanced topics such as I/O buffer circuits for ensuring reliable chip to chip communication, I/O buffer behavioural modelling, multiport empirical models for memory interfaces, compact behavioural modelling for memristive devices and resource reservation modelling for distributed embedded systems. The respective chapters detail new research findings, new models, algorithms, implementations and simulations of the above mentioned topics. As such the book will help both graduate students and researchers understand the latest research into real time modelling and processing.

Analog Circuit Design Arthur H.M. van Roermund, Herman Casier, Michiel Steyaert, 2006-12-18 Analog Circuit Design contains in total 18 tutorials. They reflect the contributions of 6 experts in each of the three fields covered by the three chapters mentioned in the subtitle as presented at the 15th workshop on Advances in Analog Circuit Design AACD held in Maastricht April 2006. This book is number 15 in this successful series of Analog Circuit Design providing valuable information and excellent overviews of analog circuit design and related CAD mainly in the fields of basic analog modules, mixed signal electronics, AD and DA converters, RF systems and automotive electronics. Analog Circuit Design is an essential

reference source for analog circuit designers and researchers wishing to keep abreast with the latest developments in the field The tutorial coverage also makes it suitable for use in an advanced design course

Simulation and Modeling Methodologies, Technologies and Applications Nuno Pina, Janusz Kacprzyk, Joaquim Filipe, 2012-10-12 The present book includes extended and revised versions of a set of selected papers from the 1st International Conference on Simulation and Modeling Methodologies Technologies and Applications SIMULTECH 2011 which was sponsored by the Institute for Systems and Technologies of Information Control and Communication INSTICC and held in Noordwijkerhout The Netherlands SIMULTECH 2011 was technically co sponsored by the Society for Modeling Simulation International SCS GDR I3 Lionphant Simulation and Simulation Team and held in cooperation with ACM Special Interest Group on Simulation and Modeling ACM SIGSIM and the AIS Special Interest Group of Modeling and Simulation AIS SIGMAS

Proceedings of 3rd Electronics Packaging Technology Conference (EPTC 2000) Thiam Beng Lim, 2000 Topics covered by this title include packaging materials packaging trends thermal design and modelling solder joint metallurgy process and reliability modelling thermal characterization materials characterization techniques and assembly manufacturing technologies

EDN ,1995 □□□□□□ □ ,2001 *Proceedings* ,1997 *New Trends in Software Methodologies, Tools and Techniques* H. Fujita, V. Mařík, 2009-08-31 Software is an essential enabler for science and the new economy but software often falls short of our expectations remaining expensive and not yet sufficiently reliable for a constantly changing and evolving market This publication which forms part of the SoMeT series consists of 41 papers carefully reviewed and revised on the basis of technical soundness relevance originality significance and clarity These explore new trends and theories which illuminate the direction of developments which may lead to a transformation of the role of software in tomorrow s global information society The book offers an opportunity for the software science community to think about where they are today and where they are going The emphasis has been placed on human centric software methodologies end user development techniques and emotional reasoning for an optimally harmonised performance between the design tool and the user The handling of cognitive issues in software development and the tools and techniques related to this form part of the contribution to this book Other comparable theories and practices in software science including emerging technologies essential for a comprehensive overview of information systems and research projects are also addressed This work represents another milestone in mastering the new challenges of software and its promising technology and provides the reader with new insights inspiration and concrete material to further the study of this new technology

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