

Chapter 11

11.1. Label the wires in the circuit of Figure P11.1 as follows:

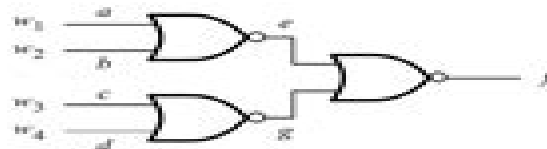


A complete fault table is

Test $w_1 w_2 w_3$	Fault detected									
	$a/0$	$a/1$	$b/0$	$b/1$	$c/0$	$c/1$	$d/0$	$d/1$	$f/0$	$f/1$
000								✓	✓	
001					✓		✓		✓	✓
010								✓	✓	
011		✓			✓		✓		✓	✓
100								✓	✓	
101				✓	✓		✓		✓	✓
110									✓	
111	✓		✓			✓			✓	

A minimal test set must include the tests $w_1 w_2 w_3 = 011, 101$, and 111 , which cover all faults except $d/1$. The latter fault can be detected by choosing one of $000, 010$, or 100 .

11.2. Label the wires in the circuit of Figure P11.2 as follows:



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Ed Bowker Staff, Staff Bowker, Ed



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Instructor's Solutions Manual to Accompany Fundamentals of Digital Logic with Vhdl Design Lord

Brown, Stephen D. Brown, Zvonko G. Vranesic, 1999-10-01 *EBOOK: Fundamentals of Digital Logic* Stephen Brown, Zvonko Vranesic, 2008-07-16 Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits. The text provides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism. It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips. Fundamental concepts are illustrated by using small examples which are easy to understand. Then a modular approach is used to show how larger circuits are designed. VHDL is a complex language so it is introduced gradually in the book. Each VHDL feature is presented as it becomes pertinent for the circuits being discussed. While it includes a discussion of VHDL, the book provides thorough coverage of the fundamental concepts of logic circuit design independent of the use of VHDL and CAD tools. A CD ROM containing all of the VHDL design examples used in the book as well as Altera's Quartus II CAD software is included free with every text. Digital System Design Using VHDL Rishabh Anand, 2023 The book covers the complete syllabus of subject as suggested by most of the universities in India. Generic VHDL code is taught and used throughout the book so that different companies' VHDL tools can be used if desired. Moving from the unknown in a logical manner. Subject matter in each chapter develops systematically from inception. Large number of carefully selected worked examples in sufficient details. No other reference is required. Ideally suited for self study. Digital Design Using VHDL William J. Dally, R. Curtis Harting, Tor M. Aamodt, 2016 Provides students with a system level perspective and the tools they need to understand, analyze, and design complete digital systems using VHDL. It goes beyond the design of simple combinational and sequential modules to show how such modules are used to build complete systems reflecting digital design in the real world. *Fundamentals of Digital Logic with VHDL Design* Stephen Brown, Zvonko Vranesic, 2022 Fundamentals of Digital Logic with VHDL Design is intended for an introductory course in digital logic design which is a basic course in most electrical and computer engineering programs. A successful designer of digital logic circuits needs a good understanding of the classical methods of logic design and a firm grasp of the modern design approach that relies on computer-aided design CAD tools. The main goals of this book are to teach students the fundamental concepts of classical manual digital design and to illustrate clearly the way in which digital circuits are designed today using CAD tools. This title will be available in Connect with the MHeBook but will not have SmartBook at this time. *Books in Print*, 1994 **Fundamentals of Digital Logic with VHDL Design** Stephen D. Brown, Zvonko G. Vranesic, 1999-06 This book is intended for an introductory course in digital logic design which is a basic course in most Electrical and Computer Engineering programs. The authors provide a desirable balance between classical and modern design approaches. Basic concepts are introduced using simple logic circuits which are designed by using both manual techniques and modern CAD tool-based methods. Having established the fundamental concepts, more complex

realistic circuits are then designed with the CAD tools The IEEE standard VHDL language is an integral part of design techniques used throughout the book Altera's advanced Max plus II CAD system on CD ROM0 and a series of step by step tutorials are included Subject Guide to Books in Print ,1996 **Forthcoming Books** Rose Arny,2003

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Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits The text provides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples which are easy to understand Then a modular approach is used to show how larger circuits are designed VHDL is a complex language so it is introduced gradually in the book Each VHDL feature is presented as it becomes pertinent for the circuits being discussed While it includes a discussion of VHDL the book provides thorough coverage of the fundamental concepts of logic circuit design independent of the use of VHDL and CAD tools A CD ROM containing all of the VHDL design examples used in the book as well Altera's Quartus II CAD software is included free with every text

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