

Chapter 11

11.1. Label the wires in the circuit of Figure P11.1 as follows:

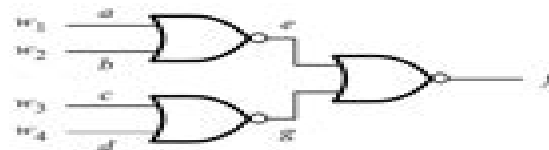


A complete fault table is

Test $w_1 w_2 w_3$	Fault detected									
	$a/0$	$a/1$	$b/0$	$b/1$	$c/0$	$c/1$	$d/0$	$d/1$	$f/0$	$f/1$
000								✓	✓	
001					✓		✓		✓	✓
010								✓	✓	
011		✓			✓		✓		✓	✓
100								✓	✓	
101				✓	✓		✓		✓	✓
110									✓	
111	✓		✓			✓			✓	

A minimal test set must include the tests $w_1 w_2 w_3 = 011, 101$, and 111 , which cover all faults except $d/1$. The latter fault can be detected by choosing one of $000, 010$, or 100 .

11.2. Label the wires in the circuit of Figure P11.2 as follows:



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Instructor's Solutions Manual to Accompany Fundamentals of Digital Logic with Vhdl Design Lord

Brown, Stephen D. Brown, Zvonko G. Vranesic, 1999-10-01 *Fundamentals of Digital Logic with Verilog Design* Stephen D.

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Chu, 2012-05-14 Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog An SoPC system on a programmable chip integrates a processor memory modules I/O peripherals and custom hardware accelerators into a single FPGA field programmable gate array device In addition to the customized software customized hardware can be developed and incorporated into the embedded system as well allowing us to configure the soft core processor create tailored I/O interfaces and develop specialized hardware accelerators for computation intensive tasks Utilizing an Altera FPGA prototyping board and its Nios II soft core processor *Embedded SoPC Design with Nios II Processor and Verilog Examples* takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board Emphasizing hardware design and integration throughout the book is divided into four major parts Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I/O peripherals including a PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides several case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology **Fundamentals of Digital Logic with VHDL Design** Stephen Brown, Zvonko Vranesic, 2022 *Fundamentals of Digital Logic with VHDL Design* is intended for an introductory course in digital logic design which is a basic course in most electrical and computer engineering programs A successful designer of

digital logic circuits needs a good understanding of the classical methods of logic design and a firm grasp of the modern design approach that relies on computer aided design CAD tools The main goals of this book are to teach students the fundamental concepts of classical manual digital design and to illustrate clearly the way in which digital circuits are designed today using CAD tools This title will be available in Connect with the MHeBook but will not have SmartBook at this time

Fundamentals of Digital Logic with Verilog Design Stephen Brown,Zvonko Vranesic,2013-03-15 Fundamentals of Digital Logic With Verilog Design teaches the basic design techniques for logic circuits It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples Use of CAD software is well integrated into the book A CD ROM that contains Altera s Quartus CAD software comes free with every copy of the text The CAD software provides automatic mapping of a design written in Verilog into Field Programmable Gate Arrays FPGAs and Complex Programmable Logic Devices CPLDs Students will be able to try firsthand the book s Verilog examples over 140 and homework problems Engineers use Quartus CAD for designing simulating testing and implementing logic circuits The version included with this text supports all major features of the commercial product and comes with a compiler for the IEEE standard Verilog language Students will be able to enter a design into the CAD system compile the design into a selected device simulate the functionality and timing of the resulting circuit implement the designs in actual devices using the school s laboratory facilities Verilog is a complex language so it is introduced gradually in the book Each Verilog feature is presented as it becomes pertinent for the circuits being discussed To teach the student to use the Quartus CAD the book includes three tutorials

Logic and Computer Design Fundamentals M. Morris Mano,Charles R. Kime,2008 Featuring a strong emphasis on the fundamentals underlying contemporary logic design using hardware description languages synthesis and verification this book focuses on the ever evolving applications of basic computer design concepts with strong connections to real world technology Treatment of logic design digital system design and computer design Ideal for self study by engineers and computer scientists

Embedded SoPC Design with Nios II Processor and VHDL Examples Pong P. Chu,2011-08-29 The book is divided into four major parts Part I covers HDL constructs and synthesis of basic digital circuits Part II provides an overview of embedded software development with the emphasis on low level I O access and drivers Part III demonstrates the design and development of hardware and software for several complex I O peripherals including PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides three case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology The book utilizes FPGA devices Nios II soft core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at www.altera.com/university The two main educational

prototyping boards are known as DE1 99 and DE2 269 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O configuration **Books in Print** ,1994 **Fundamentals Of Digital Logic With Verilog Design** STEPHEN.

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Solutions Manual -- Digital Design and Verilog HDL Fundamentals CRC Press,2008-10-20 *Fundamentals of Digital Logic with VHDL Design* Stephen D. Brown,Zvonko G. Vranesic,1999-06 This book is intended for an introductory course in digital logic design which is a basic course in most Electrical and Computer Engineering programs The authors provide a desirable balance between classical and modern design approaches Basic concepts are introduced using simple logic circuits which are designed by using both manual techniques and modern CAD tool based methods Having established the fundamental concepts more complex realistic circuits are then designed with the CAD tools The IEEE standard VHDL language is an integral part of design techniques used throughout the book Altera s advanced Max plus II CAD system on CD ROM0 and a series of step by step tutorials are included **Fundamentals of Digital Logic with VHDL Design with CD-ROM** Stephen Brown,Zvonko Vranesic,2008-04-14 Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits The text provides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples which are easy to understand Then a modular

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