

Solution Manual for Fundamentals of Logic Design 7th Edition by Roth

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(a) In both cases, if $X = 0$, the transmission is 0, and if $X = 1$, the transmission is 1.



2.4 (a) $F = [(A \cdot 1) + (A \cdot 1)] + E + BCD = A + E + BCD$

2.5 (a) $(A + B)(C + B)(D' + B)(ACD' + E)$
 $= (AC + B)(D' + B)(ACD' + E)$ By Dist. Law
 $= (ACD' + B)(ACD' + E)$ By Dist. Law
 $= ACD' + BE$ By Dist. Law

2.6 (a) $AB + CD' = (AB + C')(AB + D')$
 $= (A + C')(B + C')(A + D')(B + D')$

2.6 (c) $A'BC + EF + DEF = A'BC + E(F + DF)$
 $= A'BC + E(F + D) = (A'BC + E)(A'BC + F + D)$
 $= (A' + E)(B + E)(C + E)(A' + F + D)$

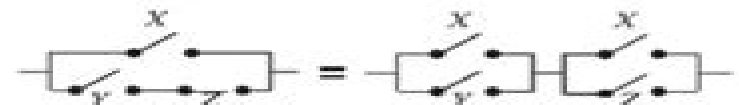
2.6 (e) 2.8 (a)

2.8 (c)

2.7 (a)



2.2 (b) In both cases, if $X = 0$, the transmission is YZ , and if $X = 1$, the transmission is 1.



2.4 (b) $Y = (AB' + (AB + B))B + A = (AB' + B)B + A$
 $= (A + B)B + A = AB + B + A = A + B$

2.5 (b) $(A' + B + C)(A' + C' + D)(B' + D')$
 $= (A' + C' + BD)(B' + D')$
 [By Distributive Law with $X = A' + C'$]
 $= A'B' + B'C' + BBD' + A'D' + C'D' + BDD'$
 $= A'B' + A'D' + C'B' + C'D'$

2.6 (b) $WX + WY'X + ZYX = X(W + WY' + ZY)$
 $= X(W + ZY)$ [By Absorption]
 $= X(W + Z)(W + Y)$

2.6 (d) $XYZ + W'Z + XQ'Z = Z(XY + W' + XQ')$
 $= Z[W' + X(Y + Q')]$
 $= Z(W' + X)(W' + Y + Q')$ By Distributive Law
 $(B \quad (AC + C') + A'C$
 $+ \quad = D'(A + C') + A'C$ By Elimination Theorem
 $F \quad = (D' + A'C)(A + C' + A'C)$
 $+ \quad = (D' + A')(D' + C')(A + C' + A')$
 $D) \quad$ By Distributive Law and Elimination Theorem
 $(C \quad = (A' + D')(C + D')$
 $+ \quad (A + B + C + D)(A + B + C + E)(A + B + C + F)$
 $F \quad = A + B + C + DEF$
 $+ \quad$ Apply second Distributive Law twice
 $D) \quad$

ACD'
 $+ CD'$
 $+ A'C$
 $= D'$

D
 E
 F

Logic Circuit Roth Solution Manual

Robert K. Dueck



Logic Circuit Roth Solution Manual:

Solutions Manual to Accompany Digital Logic Testing and Simulation Alexander Miczo, 1986 Digital Logic Design B. Holdsworth, 2014-05-12 Digital Logic Design Second Edition provides a basic understanding of digital logic design with emphasis on the two alternative methods of design available to the digital engineer This book describes the digital design techniques which have become increasingly important Organized into 14 chapters this edition begins with an overview of the essential laws of Boolean algebra K map plotting techniques as well as the simplification of Boolean functions This text then presents the properties and develops the characteristic equations of a number of various types of flip flop Other chapters consider the design of synchronous and asynchronous counters using either discrete flip flops or shift registers This book discusses as well the design and implementation of event driven logic circuits using the NAND sequential equation The final chapter deals with simple coding techniques and the principles of error detection and correction This book is a valuable resource for undergraduate students digital engineers and scientists **Fundamentals of Logic Design and Switching**

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