

the (simplest) circuit, what output waveform is produced? What is its peak amplitude? Calculate this three ways: first, use the circuit formula in Fig. 2.10a; second, use the circuit formula in Fig. 2.10b; third, use the maximum value of the input sine wave. In each case, establish a value for the peak output voltage and its location.

2.124 Using an ideal op amp, design a differentiation circuit for which the time constant is 10^{-3} s using a closed capacitor. What are the gain and phase shifts (react) for this circuit at low, mid, and high frequencies? (A low-frequency corner or roll-off occurs if the gain magnitude is high-frequency is 10^3 V/V. What is the maximum 5-dB drop? What gain and phase shift result at 50 kHz the corner frequency?)

2.125 Figure P2.125 shows a circuit that performs the high-pass single-time-constant function. Such a circuit is known as a first-order high-pass active filter. Derive the transfer function and show that the high-frequency gain is $(1 + R_2/R_1)$ and the 5-dB frequency $\omega_0 = 1/RC$. Design the circuit to provide a high-frequency gain constant of 10 dB, a high-frequency corner of 100 rad/s, and a 5-dB frequency of 10 kHz. What frequency does the magnitude of the transfer function exhibit a zero?



FIGURE P2.125

2.126 Derive the transfer function of the circuit in Fig. P2.126 (do an ideal op-amp and show that it can be written in the form

$$\frac{V_o}{V_i} = \frac{1 + R_2/R_1}{1 + sRC}$$

where $\omega_0 = 1/RC$ and $\omega_0 = 1/RC$, showing that the circuit is designed such that $\omega_0 = \omega_0$, that approximate operation for the transfer function in the following frequency ranges:

- (a) $\omega \ll \omega_0$
- (b) $\omega = \omega_0$
- (c) $\omega \gg \omega_0$

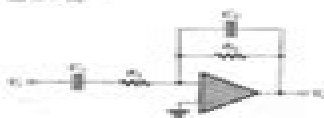


FIGURE P2.126

Use these approximations to sketch a Bode plot for the magnitude response. Observe that the corner frequency is an amplifier whose gain rolls off at the low frequency and at the corner of a high-pass Bode network, and at the high frequency and at the corner of a low-pass Bode network. Design the circuit to provide a gain of 10 dB at the "corner frequency range," a low-frequency 5-dB point at 100 Hz, a high-frequency 5-dB point at 10 kHz, and a gain constant at 10 dB of 10.

Our study of modeling the diode characteristics will lay the foundation for our study of modeling nonlinear operations in the next two chapters.

Of the many applications of diodes, their use in the design of nonlinear (or half-wave) rectifiers is (a) the most common. Therefore we shall study nonlinear circuits (in some detail) and broadly look at a number of other diode applications. Further nonlinear circuits that utilize diodes and other devices will be found throughout the book, but particularly in Chapter 5.

To understand the origin of the diode terminal characteristics, we consider its physical operation. Our study of the physical operation of the p-n junction and all the basic concepts of semiconductor physics is intended to provide a foundation for understanding not only the characteristics of junction diodes but also those of the field-effect transistors, studied in the next chapter, and the bipolar junction transistors, studied in Chapter 5.

Although most of this chapter is concerned with the study of silicon p-n junction diodes, we briefly mention some specialized diode types, including the photodiode and the light-emitting diode. The chapter concludes with a description of the diode model utilized in the SPICE circuit simulation program. We also present a design example that illustrates the use of SPICE simulation.

3.1 THE IDEAL DIODE

3.1.1 Current-Voltage Characteristics

The ideal diode may be considered the most fundamental nonlinear circuit element. It is a two-terminal device having the circuit symbol of Fig. 3.1a and the $i-v$ characteristic shown in Fig. 3.1b. The terminal characteristics of the ideal diode can be interpreted as follows: (a)



FIGURE 3.1 The ideal diode, its circuit symbol, and its $i-v$ characteristic. (a) Symbolic circuit symbol, (b) symbolic circuit symbol, (c) symbolic circuit symbol, (d) symbolic circuit symbol.

Diodes

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INTRODUCTION

In the previous chapter we dealt almost entirely with linear circuits, very nonlinearly, with no first-order nonlinear circuit elements, was considered a problem to be solved by the circuit designer. However, there are many other signal-processing functions that can be implemented only by nonlinear circuits. Examples include the generation of dc voltages from the ac power supply and the generation of signals of various waveforms (e.g., square waves, sawtooth, etc.). Also, digital logic and memory circuits constitute a special class of nonlinear circuits.

The simplest and most fundamental nonlinear circuit element is the diode. Just like a resistor, the diode has two terminals that define the device, which has a linear (straight-line) relationship between the current flowing through it and the voltage appearing across it. The diode has a nonlinear $i-v$ characteristic.

This chapter is concerned with the study of diodes. In order to understand the operation of the diode circuit, we begin with a discussion of the diode. We then introduce the diode junction diode, explain its terminal characteristics, and provide guidelines for the analysis of diode circuits. The latter task involves the important subject of device modeling.

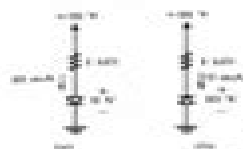


FIGURE 3.2 The two modes of operation of an ideal diode and the use of an external circuit to force the forward current (a) and the reverse voltage (b).

negative voltage (relative to the reference direction indicated in Fig. 3.1a) is applied to the diode, no current flows and the diode behaves as an open circuit (Fig. 3.1b). (When operated in this mode we call it **reverse-biased**, or represented in the reverse direction. An ideal diode has zero current when operated in the reverse direction and is said to be **cut off**, or simply **off**.)

On the other hand, if a positive voltage (relative to the reference direction indicated in Fig. 3.1a) is applied to the ideal diode, zero voltage drop appears across the diode. In other words, the ideal diode behaves as a short circuit in the forward direction (Fig. 3.1b). It passes any current with zero voltage drop. A **forward-biased** diode is said to be **conducting**, or simply **on**.

From the above description it should be noted that the external circuit must be designed to force the forward current in conducting diodes, and the external voltage across a cut-off diode, in nonconducting diodes. Figure 3.2 shows two diode circuits that illustrate this point. In the circuit of Fig. 3.2(a) the diode is obviously conducting. Thus the voltage drop will be zero, and the current through it will be determined by the $+10\text{ V}$ supply and the $1\text{ k}\Omega$ resistor in series with it. The diode is obviously cut off, and when the current will be zero, which is true because the entire $+10\text{ V}$ supply will appear across the diode.

The positive terminal of the diode is called the **anode** and the negative terminal the **cathode**, a statement from the sign of conventional diodes. The $i-v$ characteristic of the ideal diode (conducting in one direction and not in the other) should explain the choice of the arrow-like circuit symbol.

As should be evident from the preceding description, the $i-v$ characteristic of the ideal diode is highly nonlinear, although it is very simple to represent. They are as 10^3 times smaller. A nonlinear curve that consists of straight-line segments is said to be **piecewise linear**. If a device having a piecewise-linear characteristic is used in a particular application in such a way that the signal across it remains always only one of the linear segments, then the device can be considered a linear circuit element as far as that particular circuit application is concerned. On the other hand, if signals swing back and forth on more than the linear portion of the characteristic, linear analysis is no longer possible.

3.1.2 A Simple Application: The Rectifier

A fundamental application of the diode, one that makes use of its inherently nonlinear $i-v$ curve, is the rectifier circuit shown in Fig. 3.3(a). The circuit consists of the series connection

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