

MUFFAKHAM JAH COLLEGE OF ENGINEERING AND TECHNOLOGY

(Affiliated to Osmania University)

Banjara Hills, Hyderabad, Telangana State



INFORMATION TECHNOLOGY DEPARTMENT BASIC ELECTRONICS LAB MANUAL



Electronics Cad Lab Manual

Julia Schneider



Electronics Cad Lab Manual:

Architectural CAD Lab Manual Thomas L. Obermeyer, 1986 Proceedings American Society for Engineering Education. Conference, 1995 Annual Conference Proceedings American Society for Engineering Education. Conference, 1995

Industrial Education, 1990 **Lab Manual for Electronic Devices, Global Edition** THOMAS L. FLOYD, 2018-06-19

This laboratory manual is carefully coordinated to the text *Electronic Devices Tenth edition Global edition* by Thomas L Floyd. The seventeen experiments correspond to the chapters in the text except the first experiment references Chapters 1 and the first part of Chapter 2. All of the experiments are subdivided into two or three Parts. With one exception, Experiment 12 B, the Parts for the all experiments are completely independent of each other. The instructor can assign any or all Parts of these experiments and in any order. This format provides flexibility depending on the schedule, laboratory time available, and course objectives. In addition, experiments 12 through 16 provide two options for experiments. These five experiments are divided into two major sections identified as A or B. The A experiments continue with the format of previous experiments; they are constructed with discrete components on standard protoboards as used in most electronic teaching laboratories. The A experiments can be assigned in programs where traditional devices are emphasized. Each B experiment has a similar format to the corresponding A experiment but uses a programmable Analog Signal Processor (ASP) that is controlled by free Computer Aided Design (CAD) software from the Anadigm company (www.anadigm.com). These experiments support the Programmable Analog Design feature in the textbook. The B experiments are also subdivided into independent Parts, but Experiment 12 B Part 1 is a software tutorial and should be performed before any other B experiments. This is an excellent way to introduce the ASP technology because no other hardware is required other than a computer running the downloaded software. In addition to Experiment 12 B, the first 13 steps of Experiment 15 B Part 2 are also tutorial in nature for the AnadigmFilter program. This is an amazing active filter design tool that is easy to learn and is included with the AnadigmDesigner2 AD2 CAD software. The ASP is part of a Programmable Analog Module (PAM) circuit board from the Servenger company (www.servenger.com) that interfaces to a personal computer. The PAM is controlled by the AD2 CAD software from the Anadigm company website. Except for Experiment 12 B Part 1, it is assumed that the PAM is connected to the PC and AnadigmDesigner2 is running. Experiment 16 B Part 3 also requires a spreadsheet program such as Microsoft Excel. The PAM is described in detail in the Quick Start Guide Appendix B. Instructors may choose to mix A and B experiments with no loss in continuity depending on course objectives and time. We recommend that Experiment 12 B Part 1 be assigned if you want students to have an introduction to the ASP without requiring a hardware purchase. A text feature is the Device Application (DA) at the end of most chapters. All of the DAs have a related laboratory exercise using a similar circuit that is sometimes simplified to make laboratory time as efficient as possible. The same text icon identifies the related DA exercise in the lab manual. One issue is the trend of industry to smaller surface mount devices which are very difficult to work with and

are not practical for most lab work For example almost all varactors are supplied as surface mount devices now In reviewing each experiment we have found components that can illustrate the device function with a traditional one The traditional through hole MV2109 varactor is listed as obsolete but will be available for the foreseeable future from Electronix Express www.elexp.com so it is called out in Experiment 3 All components are available from Electronix Express www.elexp.com as a kit of parts see list in Appendix A The format for each experiment has not changed from the last edition and is as follows

Introduction A brief discussion about the experiment and comments about each of the independent Parts that follow

Reading assignment in the Floyd text related to the experiment

Key Objectives A statement specific to each Part of the experiment of what the student should be able to do

Components Needed A list components and small items required for each Part but not including the equipment found at a typical lab station

Particular care has been exercised to select materials that are readily available and reusable keeping cost at a minimum

Parts There are two or three independent parts to each experiment

Needed tables graphs and figures are positioned close to the first referenced location to avoid confusion

Step numbering starts fresh with each Part but figures and tables are numbered sequentially for the entire experiment to avoid multiple figures with the same number

Conclusion At the end of each Part space is provided for a written conclusion

Questions Each Part includes several questions that require the student to draw upon the laboratory work and check his or her understanding of the concepts

Troubleshooting questions are frequently presented

Multisim Simulation At the end of each A experiment except 1 one or more circuits are simulated in a Multisim computer simulation

New Multisim troubleshooting problems have been added to this edition

Multisim troubleshooting files are identified with the suffix f1 f2 etc in the file name standing for fault1 fault2 etc

Other files with nf as the suffix include demonstrations or practice using instruments such as the Bode Plotter and the Spectrum Analyzer

A special icon is shown with all figures that are related to the Multisim simulation

Multisim files are found on the website www.pearsonglobaledition.com

Floyd Microsoft PowerPoint slides are available at no cost to instructors for all experiments

The slides reinforce the experiments with troubleshooting questions and a related problem and are available on the instructor s resource site

Each laboratory station should contain a dual variable regulated power supply a function generator a multimeter and a dual channel oscilloscope

A list of all required materials is given in Appendix A along with information on acquiring the PAM

As mentioned components are also available as a kit from Electronix Express the kit number is 32DBEDFL10

School Shop ,1986

Practical Guide to the Packaging of Electronics Ali Jamnia,2016-12-01 Successfully Estimate the Thermal and Mechanical Characteristics of Electronics Systems A definitive guide for practitioners new to the field or requiring a refresher course

Practical Guide to the Packaging of Electronics Thermal and Mechanical Design and Analysis Third Edition provides an understanding of system failures and helps identify the areas where they can occur

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new system or troubleshoot a current one Updated to reflect recent developments in the field this latest edition adds two new chapters on acoustic and reliability fundamentals and contains more information on electrical failures and causes It also includes tools for understanding heat transfer shock and vibration Additionally the author Addresses various cross discipline issues in the design of electromechanical products Provides a solid foundation for heat transfer vibration and life expectancy calculations Identifies reliability issues and concerns Develops the ability to conduct a more thorough analysis for the final design Includes design tips and guidelines for each aspect of electronics packaging Practical Guide to the Packaging of Electronics Thermal and Mechanical Design and Analysis Third Edition explains the mechanical and thermal fluid aspects of electronic product design and offers a basic understanding of electronics packaging design issues Defining the material in depth it also describes system design guidelines and identifies reliability concerns for practitioners in mechanical electrical or quality engineering *Electronic Learning* ,1986 *Robotics, CAD/CAM Market Place*, 1985 ,1985 *Electronic Design Automation Frameworks* Tom Rhyne,1992 Many of the advances achieved in framework technology during the last five years are reported in this volume However despite acknowledged developments and an enormous investment by the Computer Aided Design CAD vendor industry and others commercial framework products have been slow to appear on the market Further those which have appeared have largely failed to meet original targets whether in terms of scope or performance or both Reaching a consensus on new international standards has been a painfully slow process with rapid advances in technology often rendering new standards out of date even before their eventual appearance A motivation for agreement on technical issues not yet fully understood or researched will be vital if a commercial basis to underpin future development is to be achieved It is hoped this book will stimulate interchange between researchers developers and users so that practical progress can be made backed by the strong support of interested industries **El-Hi Textbooks & Serials in Print, 2005** ,2005 **Principles of Electronic Circuits** Stanley G. Burns,Paul R. Bond,1997 Accompanying CD ROM includes Evaluation version of PSPICE SPICE netlists Electronic Workbench circuit models and Acrobat transparencies *Electronics Engineers' Handbook* Donald G. Fink,Donald Christiansen,1989 Very Good No Highlights or Markup all pages are intact **Digital Electronics Laboratory Experiments** James W. Stewart,Chao-Ying Wang,2001 For Freshman Sophomore level courses in Digital Circuit Design Digital System Design and Computer Engineering Technology This book offers an easy to read easy to follow approach to digital fundamentals through the use of Complex Programmable Logic Devices CPLDs The use of advanced logic device technology in an introductory digital course prepares students both for lab work in advanced courses as well as for using an industry standard design environment *Electronics* ,1991 **Computer Hardware Description Languages and their Applications** D. Agnew,L. Claesen,R. Camposano,2014-05-21 Hardware description languages HDLs have established themselves as one of the principal means of designing electronic systems The interest in and usage of HDLs continues to spread rapidly driven by the increasing complexity of systems the growth of HDL

driven synthesis the research on formal design methods and many other related advances This research oriented publication aims to make a strong contribution to further developments in the field The following topics are explored in depth BDD based system design and analysis system level formal verification formal reasoning on hardware languages for protocol specification VHDL HDL based design methods high level synthesis and text graphical HDLs There are short papers covering advanced design capture and recent work in high level synthesis and formal verification In addition several invited presentations on key issues discuss and summarize recent advances in real time system design automatic verification of sequential circuits and languages for protocol specification

The Technology Teacher ,1997 **El-Hi Textbooks & Serials in Print, 2000** ,2000 *Electronic Design Automation Frameworks* F. J. Rammig,Ron Waxman,1991 During the last decade the field of electronic design automation has changed from a small industry offering a random sampling of commercial and academic design automation tools to a significant industry comprising offerings ranging from individual tools to total design systems all based upon a set of emerging standards Workers in electronic design automation are active in the development of integrated design environments and therefore in the development of frameworks Frameworks for electronic design automation are rather similar to those for mechanical or software engineering however the class of tools may differ resulting in differing specific requirements for the services to be offered The present book documents the results of the 2nd Workshop on electronic design automation frameworks The question of standardization is of special interest within the book especially as related to VHDL EDIF PDES and CFI Also included are discussions of the role of specialized languages for specific environments and how the user community can help standards to evolve

AIAA Space Programs and Technologies Conference ,1994

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