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S.Y. B.Sc. (Computer Science) (Semester – II) Examination, 2013
ELC – 221 : ELECTRONICS (Paper – I)
Microcontroller and Embedded Systems
(2008 Pattern)

Time : 2 Hours

Max. Marks : 40

Instructions : 1) All questions are compulsory.
2) Figures to the right indicate full marks.
3) Neat diagrams must be drawn wherever necessary.

1. Answer the following in **one** or **two** sentences : (1×10=10)
- a) What is the address stored in stack pointer and program counter after 8051 is reset ?
 - b) Which address of internal RAM is used for bit addressing ?
 - c) Which port pins are used for accessing external interrupts ?
 - d) What will be the effect of instruction
back : JNB P1.5, back
end.
 - e) If T_{H} register in Mode 2 operation is loaded with – 50, then after how many clock pulses the timer overflow flag will be set ?
 - f) Give the vector address for Timer-0 and serial communication interrupts.
 - g) Mention the role of line drivers in serial communication.
 - h) Define Emulator.
 - i) Define IDE.
 - j) Give the standard reference current for DAC 0808.

Embedded Systems Question Paper For Eie

H Kauffman



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Table of Contents Embedded Systems Question Paper For Eie

1. Understanding the eBook Embedded Systems Question Paper For Eie
 - The Rise of Digital Reading Embedded Systems Question Paper For Eie
 - Advantages of eBooks Over Traditional Books
2. Identifying Embedded Systems Question Paper For Eie
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Embedded Systems Question Paper For Eie
 - User-Friendly Interface
4. Exploring eBook Recommendations from Embedded Systems Question Paper For Eie
 - Personalized Recommendations
 - Embedded Systems Question Paper For Eie User Reviews and Ratings
 - Embedded Systems Question Paper For Eie and Bestseller Lists
5. Accessing Embedded Systems Question Paper For Eie Free and Paid eBooks

- Embedded Systems Question Paper For Eie Public Domain eBooks
 - Embedded Systems Question Paper For Eie eBook Subscription Services
 - Embedded Systems Question Paper For Eie Budget-Friendly Options
6. Navigating Embedded Systems Question Paper For Eie eBook Formats
 - ePub, PDF, MOBI, and More
 - Embedded Systems Question Paper For Eie Compatibility with Devices
 - Embedded Systems Question Paper For Eie Enhanced eBook Features
 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Embedded Systems Question Paper For Eie
 - Highlighting and Note-Taking Embedded Systems Question Paper For Eie
 - Interactive Elements Embedded Systems Question Paper For Eie
 8. Staying Engaged with Embedded Systems Question Paper For Eie
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Embedded Systems Question Paper For Eie
 9. Balancing eBooks and Physical Books Embedded Systems Question Paper For Eie
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Embedded Systems Question Paper For Eie
 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
 11. Cultivating a Reading Routine Embedded Systems Question Paper For Eie
 - Setting Reading Goals Embedded Systems Question Paper For Eie
 - Carving Out Dedicated Reading Time
 12. Sourcing Reliable Information of Embedded Systems Question Paper For Eie
 - Fact-Checking eBook Content of Embedded Systems Question Paper For Eie
 - Distinguishing Credible Sources
 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development

- Exploring Educational eBooks
14. Embracing eBook Trends
- Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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