

8085 - Instruction set list

8085 Instructions - Datatransfer

Opcode	Operand	Explanation of Instruction	Description
MOV	Rd, Rs M, Rs Rd, M	Copy from source(Rs) to destination(Rd)	This instruction copies the contents of the source register into the destination register; the contents of the source register are not altered. If one of the operands is a memory location, its location is specified by the contents of the HL registers. Example: MOV B, C or MOV B, M
MVI	Rd, data M, data	Move immediate 8-bit	The 8-bit data is stored in the destination register or memory. If the operand is a memory location, its location is specified by the contents of the HL registers. Example: MVI B, 57H or MVI M, 57H
LDA	16-bit address	Load accumulator	The contents of a memory location, specified by a 16-bit address in the operand, are copied to the accumulator. The contents of the source are not altered. Example: LDA 2034H
LDAX	B/D Reg-pair	Load accumulator indirect	The contents of the designated register pair point to a memory location. This instruction copies the contents of that memory location into the accumulator. The contents of either the register pair or the memory location are not altered. Example: LDAX B
LXI	Reg. pair, 16-bit data	Load register pair immediate	The instruction loads 16-bit data in the register pair designated in the operand. Example: LXI H, 2034H or LXI H, XYZ
LHLD	16-bit address	Load H and L registers direct	The instruction copies the contents of the memory location pointed out by the 16-bit address into register L and copies the contents of the next memory location into register H. The contents of source memory locations are not altered. Example: LHLD 2040H

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Table of Contents Instruction Set Of 8085 With Examples

1. Understanding the eBook Instruction Set Of 8085 With Examples
 - The Rise of Digital Reading Instruction Set Of 8085 With Examples
 - Advantages of eBooks Over Traditional Books
2. Identifying Instruction Set Of 8085 With Examples
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Instruction Set Of 8085 With Examples
 - User-Friendly Interface
4. Exploring eBook Recommendations from Instruction Set Of 8085 With Examples
 - Personalized Recommendations

- Instruction Set Of 8085 With Examples User Reviews and Ratings
- Instruction Set Of 8085 With Examples and Bestseller Lists
- 5. Accessing Instruction Set Of 8085 With Examples Free and Paid eBooks
 - Instruction Set Of 8085 With Examples Public Domain eBooks
 - Instruction Set Of 8085 With Examples eBook Subscription Services
 - Instruction Set Of 8085 With Examples Budget-Friendly Options
- 6. Navigating Instruction Set Of 8085 With Examples eBook Formats
 - ePub, PDF, MOBI, and More
 - Instruction Set Of 8085 With Examples Compatibility with Devices
 - Instruction Set Of 8085 With Examples Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Instruction Set Of 8085 With Examples
 - Highlighting and Note-Taking Instruction Set Of 8085 With Examples
 - Interactive Elements Instruction Set Of 8085 With Examples
- 8. Staying Engaged with Instruction Set Of 8085 With Examples
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Instruction Set Of 8085 With Examples
- 9. Balancing eBooks and Physical Books Instruction Set Of 8085 With Examples
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Instruction Set Of 8085 With Examples
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Instruction Set Of 8085 With Examples
 - Setting Reading Goals Instruction Set Of 8085 With Examples
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Instruction Set Of 8085 With Examples
 - Fact-Checking eBook Content of Instruction Set Of 8085 With Examples

- Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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