

( N.B. : All questions are compulsory. )

1. (a) Give classification of memory. 10  
(b) What is latch ? Describe its function.
2. Attempt any three of the following :— 15
  - (a) Distinguish between SRAM and DRAM.
  - (b) Explain encoder with suitable example.
  - (c) Explain tri-state buffer in detail.
  - (d) Explain related to memory :—
    - (i) Expanding memory. (ii) Expanding word size.
3. Attempt any three of the following :— 15
  - (a) State functions of ALU of 8085.
  - (b) What is flag ? List and Describe the flags of 8085.
  - (c) Write short note on 8085 Address, Data and Control Bus.
  - (d) State and describe the interrupt signal available in 8085.
4. Attempt any three of the following :— 15
  - (a) Classify the instructions on the basis of byte length with example.
  - (b) Write an assembly language program to find the smallest number from memory block starting from 2000 H to 2005 H and store largest number in 2020 H.
  - (c) Explain following instructions in 8085 :—
 

(i) ADI Data 8	(iv) J-condition address
(ii) STAX rp	(v) RAR.
(iii) ANA r	
  - (d) Explain the programming model of 8085.
5. Attempt any three of the following :— 15
  - (a) What are the benefits of Raid ?
  - (b) What is cache memory ? Why it is needed ?
  - (c) What is fetch, Execute and Instruction cycle ?
  - (d) Explain the features of PCI Bus.
6. Attempt any three of the following :— 15
  - (a) Explain the features of 8051 micro-controller.
  - (b) Explain all 8051 flags of micro-controller.
  - (c) Explain the operations of stack in 8051 micro-controller.
  - (d) Explain how will you select internal memory bank with suitable example.
7. Attempt any three of the following :— 15
  - (a) Explain the features of Input/output ports of 8051.
  - (b) Explain various addressing modes of 8051.
  - (c) Explain the following instructions of 8051 :—
 

(i) MOVX A, Rp	(iv) MUL AB
(ii) INC A	(v) RLA.
(iii) Add R <sub>i</sub> R <sub>j</sub> .	
  - (d) Write a program to perform in 8051 BCD addition of two-8bit numbers. Data is stored in data memory location 51H, 52H and store the result in data memory. Location 53H and carry in 54H.

# Model Question Paper Of Advanced Microprocessor 2013

**Phil Brown**



## **Model Question Paper Of Advanced Microprocessor 2013:**

Microprocessor and Microcontroller Saurabh Chaudhury, Risha Mal, 2025-06-01      Microprocessors & Microcontrollers Atul P. Godse, Dr. Deepali A. Godse, 2021-01-01 The book is written for an undergraduate course on the 8086 microprocessor and 8051 microcontroller. It provides comprehensive coverage of the hardware and software aspects of 8086 microprocessor and 8051 microcontroller. The book is divided into three parts. The first part focuses on 8086 microprocessor. It teaches you the 8086 architecture, instruction set, Assembly Language Programming, ALP interfacing 8086 with support chips, memory and peripherals such as 8251, 8253, 8255, 8259, 8237 and 8279. It also explains the interfacing of 8086 with data converters ADC and DAC and introduces a traffic light control system. The second part focuses on multiprogramming and multiprocessor configurations, numeric processor 8087, I/O processor 8089 and introduces features of advanced processors such as 80286, 80386, 80486 and Pentium processors. The third part focuses on 8051 microcontroller. It teaches you the 8051 architecture, instruction set, programming 8051 and interfacing 8051 with external memory. It explains timers, counters, serial port, interrupts of 8051 and their programming. It also describes the interfacing 8051 with data converters ADC and DAC, keyboards, LCDs, LEDs, stepper motors and sensors.      Advanced Computer Architecture Rajiv Chopra, 2008 This book covers the syllabus of GGSIPU, DU, UPTU, PTU, MDU, Pune University and many other universities. It is useful for B.Tech CSE, IT, M.Tech CSE, MCA, SE students. Many solved problems have been added to make this book more fresh. It has been divided into three parts: Parallel Algorithms, Parallel Programming and Super Computers.      *ACCA P5 - Advanced Performance Management - Study Text 2013* BPP Learning Media, 2011-12-15 This examiner reviewed Study Text covers all that you need to know for P5. It features plenty of recent case studies illustrating key syllabus areas and questions to hone your understanding of what you have just read. This paper tests your application of knowledge so these studies and questions are key learning tools. You will also find up-to-date information on the latest management theories and techniques which feature highly in this paper. Then there are plenty of exam tips to guide your study and help you focus on what is essential to know. Now it's up to you.      **Advanced HPC-based Computational Modeling in Biomechanics and Systems Biology** Mariano Vázquez, Peter V. Coveney, Hernan Edgardo Grecco, Alfons Hoekstra, Bastien Chopard, 2019-04-04 This eBook is a collection of articles from a Frontiers Research Topic. Frontiers Research Topics are very popular trademarks of the Frontiers Journals Series; they are collections of at least ten articles all centered on a particular subject. With their unique mix of varied contributions from Original Research to Review Articles, Frontiers Research Topics unify the most influential researchers, the latest key findings and historical advances in a hot research area. Find out more on how to host your own Frontiers Research Topic or contribute to one as an author by contacting the Frontiers Editorial Office: frontiersin.org/about/contact.      **Advanced Information Systems Engineering** Schahram Dustdar, Eric Yu, Camille Salinesi, Dominique Rieu, Vik Pant, 2020-06-02 This book constitutes the refereed proceedings of the 32nd International Conference on Advanced

Information Systems Engineering CAiSE 2020 held in Grenoble France in June 2020 The 33 full papers presented in this volume were carefully reviewed and selected from 185 submissions The book also contains one invited talk in full paper length The papers were organized in topical sections named distributed applications AI and big data in IS process mining and analysis requirements and modeling and information systems engineering Abstracts on the CAiSE 2020 tutorials can be found in the back matter of the volume The conference was held virtually due to the COVID 19 pandemic

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**Information Science in Action: System Design** Anthony Debons,Arvid G. Larson,2013-11-11      **Human-Machine**

**Interactive Systems** Allen Klinger, 2013-03-08 Many hardware devices present either results or alternatives selected by computers to users. A few are video display terminals (VDTs), touch tone telephones, and computer generated speech systems. In part, this book concerns the impact and implications of such tools. Alternatively, this is an attempt to provide material for researchers, students, and managers concerned with computer interfaces. The subject of computer interfaces is at one level a technical subarea sharing common interests with the broad disciplines of computer science, psychology, and bioengineering. However, it is also a topic thrust to the forefront of interest of a wide variety of individuals who confront one of the most striking technological changes that has occurred in human history: the introduction of contact with computing devices as an essential component of many kinds of ordinary transactions. Point of entry sales, travel, and entertainment reservations, and library information are commonly conducted today by interaction with digital calculating devices that did not exist in the recent past. The papers in this book present several concerns arising from the widespread use of computing. One involves the future implications of further advances of this technology. This is a twofold issue: a) the potential consequences of changing the basic way that information is managed in areas ranging from design, engineering, and management planning to information access, education, and clerical function; and b) improvements that could be instituted from further development of the special characteristics of display techniques, technologies, and algorithms. *Government Reports, Announcements & Index*, 1984

Leviticus Daniel Seltzer, 2013-08-30 Science has created a world where anything is possible and everything is affordable. A world where illness and disease have been eradicated. What if you could be young forever? What if you didn't want to? Levi Clayton Furstman's decision not to be inoculated with technology designed to bestow youth and immortality leads him on a journey that forces him to reexamine his relationships, his purpose in life, and ultimately what it means to be human.

Electronics and Computer Acronyms Phil Brown, 2013-09-03 *Electronics and Computer Acronyms* presents a list of almost 2,500 acronyms related to electronics and computers. The material for this book is drawn from a number of subject areas including electrical electronics, computers, telecommunications, fiber optics, microcomputers, microprocessors, audio, video, and information technology. The acronyms also encompass avionics, military data processing, instrumentation, units, measurement standards, services, organizations, associations, and companies. This dictionary offers a comprehensive and broad view of electronics and all that is associated with it. Cross references are provided in the form of abbreviations within parentheses. For example, the entry for CCD (charge coupled device) provides three cross references: charge transfer device (CTD), metal oxide semiconductor (MOS), and capacitor (C). To broaden further the explanation, if required, referral to entries CTD, MOS, and C part 6 C is advised. The system of prefixes and units used in the *Système International d'Unités* is followed throughout the book. This monograph is intended for students, engineers, and technicians as well as those with an interest in computers and electronics.

*Intellectual Property Rights* Mario Cimoli, Giovanni Dosi, Keith E. Maskus, Ruth L. Okediji, Jerome H. Reichman, Joseph E. Stiglitz, 2014 This book analyses the impact of diverse intellectual

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