



### III SEMESTER B.TECH (ELECTRICAL & ELECTRONICS ENGINEERING)

### END SEMESTER EXAMINATIONS, NOVEMBER 2018

### SUBJECT: DIGITAL ELECTRONIC CIRCUITS [ELE 2102]

REVISED CREDIT SYSTEM

Time: 3 Hours

Date: 24, November 2018

Max. Marks: 50

#### Instructions to Candidates:

- ❖ Answer ALL the questions.
- ❖ Missing data may be suitably assumed.

- 1A. Given the network of Fig Q1A, determine the functions  $f_2$  and  $f_3$  if  $f_1 = B$  and the overall function is to be

$$F(A,B,C,D) = \prod M(1,3,9,11,12,13,14) + d(2,8)$$

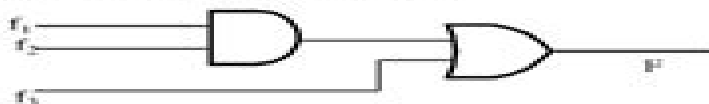


Fig Q1A

(03)

- 1B. Show how  $F = A + B + \bar{C}$  can be implemented with one 2 input NAND gate and one 2 input NOR gate.

(02)

- 1C. Obtain a minimal SOP expression using Tabular method for the following equation.

$$F(A, B, C, D) = \sum m(2, 4, 7, 9, 11, 12, 14) + d(0, 5, 6, 13, 15)$$

(05)

- 2A. Design a 2 bit comparator circuit as shown in the Fig Q2A using 1:16 D-mux and residual - gates



Fig Q2A

(04)

- 2B. Write the truth table and design BCD to Seven-segment decoder circuit using common anode configuration

(04)

- 2C. Implement , 8-bit addition and subtraction operation using IC 74LS283

(02)

- 3A. Design asynchronous mod-16 up/down counter using positive edge triggered T Flip-flops. Draw the waveform for the same.

(03)

- 3B. Design a one minute counter using decade 7490 IC's

(03)

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### Table of Contents Electronics Exam Papers

1. Understanding the eBook Electronics Exam Papers
  - The Rise of Digital Reading Electronics Exam Papers
  - Advantages of eBooks Over Traditional Books
2. Identifying Electronics Exam Papers
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Electronics Exam Papers
  - User-Friendly Interface
4. Exploring eBook Recommendations from Electronics Exam Papers
  - Personalized Recommendations
  - Electronics Exam Papers User Reviews and Ratings
  - Electronics Exam Papers and Bestseller Lists

5. Accessing Electronics Exam Papers Free and Paid eBooks
  - Electronics Exam Papers Public Domain eBooks
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  - Electronics Exam Papers Budget-Friendly Options
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  - Electronics Exam Papers Enhanced eBook Features
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  - Adjustable Fonts and Text Sizes of Electronics Exam Papers
  - Highlighting and Note-Taking Electronics Exam Papers
  - Interactive Elements Electronics Exam Papers
8. Staying Engaged with Electronics Exam Papers
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Electronics Exam Papers
9. Balancing eBooks and Physical Books Electronics Exam Papers
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Electronics Exam Papers
10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
11. Cultivating a Reading Routine Electronics Exam Papers
  - Setting Reading Goals Electronics Exam Papers
  - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Electronics Exam Papers
  - Fact-Checking eBook Content of Electronics Exam Papers
  - Distinguishing Credible Sources
13. Promoting Lifelong Learning

- Utilizing eBooks for Skill Development
  - Exploring Educational eBooks
14. Embracing eBook Trends
- Integration of Multimedia Elements
  - Interactive and Gamified eBooks

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