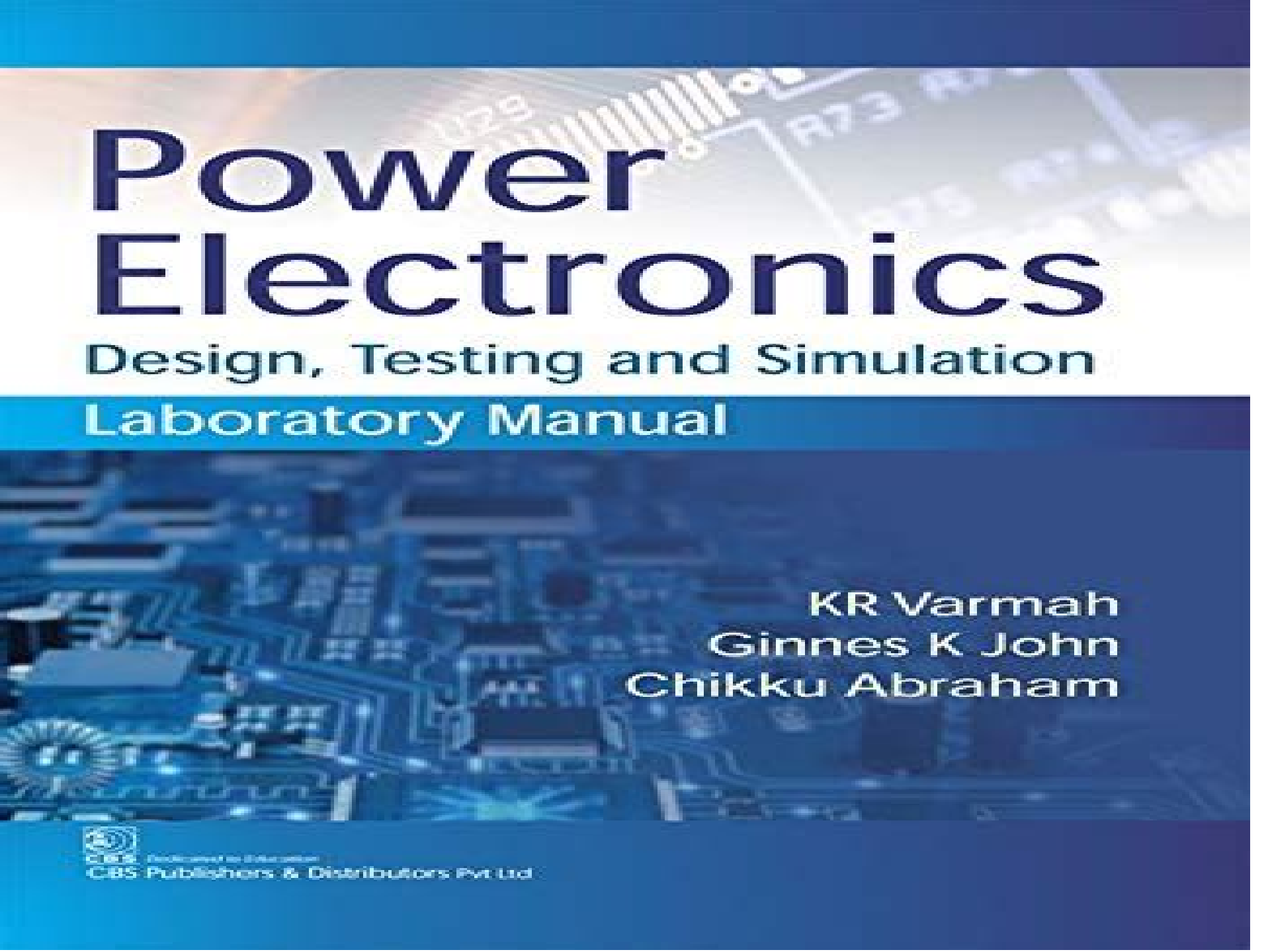




Power Electronics

Design, Testing and Simulation
Laboratory Manual

KR Varmah
Ginnes K John
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