



Reg. No. : .....

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**Third Semester B.Tech. Engineering Degree (Regular/Supplementary)**  
**Examination, November 2010**  
**2K6 CS/IT 305 : SWITCHING THEORY AND LOGIC DESIGN**

Time: 3 Hours

Max. Marks: 100

*Instruction : Answer all questions.*

1. a) Give symbols and truth tables of electronic gates and contact networks.  
b) Define sum of products and product of sum with examples.  
— c) What are universal gates and its properties ? Give suitable example.  
d) Write a note on look ahead adders.  
e) What do you mean by path sensitisation ?  
f) What do you mean by programmable logic arrays ? Give example.  
g) Write a note on Flip-flops. Give truth table for SR and JK Flip-flops.  
h) What do you mean by synchronous counter ? How it differs from asynchronous counter ?  
(5×8=40)
2. A) i) Simplify following K-map using sum of products.  
10

AB → CD ↓	00	01	11	10
00	1		1	1
01	1			
11	1		1	1
10	1		1	1

verify using truth table.

- ii) What do you mean by normal and canonical forms ? Give suitable example.  
5

OR

# High Speed Switching Architecture University Question Paper

**M Mark**



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